

Format of Application

Electronics and ICT Academy
One Week National Level
Faculty Development Program

on
**Advanced Digital Signal Processing
and Applications**
8th - 13th November, 2016

1. Name :
2. Designation :
3. Institution :
4. E-Mail :
5. DD No. : Bank : Date:
6. Address for Correspondence:
7. Educational Qualifications with specialization :
8. Subjects taught so far:
9. No. of refresher courses/workshops attended:
10. Experience (in years)

Teaching:	Research:	Industry:
-----------	-----------	-----------
11. Accommodation required : YES / NO
12. Do you belong to SC/ST : YES/NO

DECLARATION

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place : _____
Date: _____ Signature of the Applicant

SPONSORSHIP CERTIFICATE

Dr. / Mr. / Ms. _____ is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **Advanced Digital Signal Processing and Applications** Sponsored by Electronics and ICT Academy during **8th - 13th November, 2016** at MVGR College of Engineering(A), Vizianagaram, AP.

Place: _____
Date: _____ Signature of the
Head of the Institution with seal

Sri. P. Ashok Gajapathi Raju
Chairman, MANSAS

Patron

Dr. K.V.L.Raju
Principal

President

Advisory Board

Dr. D. R. K Raju
Prof. D.J.J.Ramachandra Raju
Dr. Y. M.C Sekhar
Dr. D R Prasada Raju
Dr. P.Ravindranath
Dr. M.Sunil Prakash
Dr. K. Rajeswara Rao
Dr. R. Ramesh
Dr. S. Srinivasa Rao
Dr. T.V.N. Partha Sarathi

Correspondent, MANSAS
Vice-Principal (Admin)
Vice-Principal (Academics)
Dean (Faculty Development)
Dean (Strategic Planning)
Dean (Training & Placements)
Dean (Civil Infrastructure)
Dean (Research & Development)
Dean (Student Affairs)
Dean (Examinations)

Organizing Committee

Dr. S. M Vali , Professor
Ms. P.U K Prabha, Assoc. Prof
Ms.D.Ramadevi, Assoc. Prof
Dr.G.Anjaneyulu, Assoc. Prof
Dr.M.Satyanarayana, Assoc. Prof
Mr.P.Surya Prasad, Assoc. Prof
Dr. T.A.N.S.N Varma, Assoc. Prof
Ms. V. Lavanya, Asst. Prof
Mr.V.N.L Kumar, Asst. Prof
Ms.M. L.Prasanna Rani, Asst. Prof
Ms.G. Vimala Kumari, Asst. Prof
Ms. K. Saritha Raj, Asst. Prof
Mr. N. Shanmukha Rao, Asst. Prof
Ms. U.N.Subadhra Devi, Asst. Prof
Mr.K.Rakesh, Asst. Prof
Mr.M.Vinodh Kumar, Asst. Prof
Mr. B.Srinivas, Asst. Prof
Ms. B Lavanya, Asst. Prof

Mr. D.Raja Ramesh, Asst. Prof
Mr.K.Satyanarayana Raju, Asst. Prof
Mr. U.V.Sitarama Varma, Asst. Prof
Mr. P.Srikanth, Asst. Prof
Mr. P. Santosh Kumar, Asst. Prof
Ms. H.Sudha, Asst. Prof
Mr. A.Ashok Kumar, Asst. Prof
Mr. J. Bhaskara Rao, Asst. Prof
Mr. M. Nagendra Kumar, Asst. Prof
Mr. V. S. M. S. Varma, Asst. Prof
Mr. K. V. Koteswara Rao, Asst. Prof
Ms. A. Sravani, Asst. Prof
Mrs. D.H.Rachel , Asst. Prof
Mrs. G.Saroja, Asst. Prof
Mrs. D.Mounika , Asst. Prof
Mrs. Harati V L Devi, Asst. Prof

Coordinators

Dr. T. Kishore Kumar
Head, Dept. of ECE,
NIT-Warangal
Warangal, TS
Mob: 8332969353
kishoret@gmail.com

Dr. R. Ramana Reddy,
Head, Dept. of ECE,
MVGR College of Engg.(A)
Vizianagaram, AP
Mob:9440899594
ecehod.mvgr@gmail.com

Address for Communication

Dr. R. Ramana Reddy, Prof. & Head, Department of ECE
MVGR College of Engineering (A) Chintalavalasa,
Vizianagaram, AP-535 005 , Ph: 08922-241037

Mail the scanned copies of filled-in and duly signed application form with DD to **ecehod.mvgr@gmail.com**,

For more information visit:

<http://nitw.ac.in/eict/> (or) www.mvgrce.edu.in



**One Week National Level
Faculty Development Program (FDP)**

on

**Advanced Digital Signal Processing
and Applications**

8th - 13th November, 2016

Jointly organized by

Electronics & ICT Academy, NIT Warangal,

Telangana State-506004

Deity, HRD division, Ministry of ICT

Govt. of India Sponsored

and

**Department of ECE,
Maharaj Vijayaram Gajapathi Raj
College of Engineering (A)**



Maharaj Vijayaram Gajapathi Raj College of Engineering(A)

Re-accredited by NBA & NAAC with 'A' grade, Approved by AICTE, New Delhi,
Permanently Affiliated to JNTU, Kakinada, Listed u/s 2(f) and 12(B) of the UGC Act 1956
**Vijayaramnagar Campus, Chintalavalasa,
Vizianagaram, AP - 535005**

Preabmle:

“Electronics & ICT Academy” is being set up at NIT Warangal with financial assistance from DeitY, MCIT, and GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa States and Pudicherry, Andaman & Nicobar Islands and Lakshadweep UTs. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Computer Science, Electronics, Information Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities.

About NIT Warangal:

National Institute of Technology, (formerly Regional Engineering College), Warangal is an Institute of National Importance and fully funded by MHRD, Govt. of India. It offers various programmes, B.Tech in eight branches, PG and M. Tech Programmes and also Ph.D. programmes.

About College

MVGR was established in the year 1997 by Maharaj Alak Narayan Society for Arts and Sciences (MANSAS). MANSAS was established in 1958 by the erstwhile Raja Saheb of Vizianagaram, (Late) Dr. PVG Raju. The college has state of the art Labs which host equipment costing Rs. 13 Crore. The college is spread on a lush green 43 Acre campus and has more than 5 lakh sft. build up area. The college boasts of its library extended over 45,000 sft. area adorning an enviable collection of books.

It offers Bachelors Degree in Civil, Chemical, Computer Science, Electronics and Communication, Electrical & Electronics, Information Technology and Mechanical Engineering which were reaccredited by NBA It also offers Post Graduate courses in Engineering and Management. CSE, Mechanical, ECE, Chemical, Civil and MBA departments are recognized as research center by JNTU-K, Kakinada to offer Ph. D programs. The college is reaccredited by NAAC with ‘A’ Grade and recognized as a Business Incubator Centre by the Ministry of Micro, Small and Medium Enterprises (MSME), Govt. of India. The college is undertaking consultancy/Research projects from AICTE, DST and Govt. of AP. etc.

About ECE Department

Department of Electronics and Communication Engineering was established in MVGR College of Engineering in the year 1997 with the objective of imparting quality education in the field of Electronics and Communication. Department is reaccredited by NBA.

Department is recognized as research center by JNTU-K, Kakinada. The Department is offering an undergraduate course in ECE with an intake of 180 and M.Tech program in VLSI.

The Department has qualified and experienced faculty members with 6 Ph.Ds and 15 pursuing Ph.Ds. Department is located in a dedicated building with 46,000 sft built up area with 14 state of art and well-equipped laboratories with latest configuration and software. The Department has conducted 13 Workshops for faculty and 6 national level symposiums for students. It had organized many expert lectures with eminent personalities from different reputed organizations.

Department is offering Certification courses / Training programs in Embedded Systems in collaboration with ThinkLABs, IIT Mumbai, NI LabVIEW Academy in collaboration with National Instruments, Bangalore and Wipro Mission10X Technology Learning Center in collaboration with Wipro Technologies, Bangalore

About the Workshop

As part of Electronics and ICT Academy initiative, a Faculty Development Programme (FDP) on Advanced Digital Signal Processing and Applications is being conducted to the faculty members of engineering colleges, industries and research laboratories.

Major Course Contents

- Signal Processing for Communications and Applications
- Transform Techniques
- Multirate Signal Processing
- Image Processing
- Speech Signal Processing
- Radar Signal Processing
- Social Signal Processing
- VLSI Signal Processing
- VLSI and DSP Architectures
- Hands-on sessions on every module

Organized at

The programme will be coordinated and conducted by Department of Electronics and Communication Engineering of MVGR College of Engineering (A), Vizianagaram, AP. The whole course is handled by academicians from premier institutions, Industry experts and Research advisors.

Eligibility

The programme is open to the faculty of Engineering and, MCA colleges and other allied disciplines in Telangana, Andhra Pradesh, Karnataka, Goa states and Andaman and Nicobar Islands, Puducherry UTs. Scientists, engineers and other working professionals are also eligible.

Registration fee particulars

Faculty members and Research Scholars from mentioned states: Rs. 2000/-

Industry participants: Rs. 6000/-

Participants from other states: Rs.8500/-

The participants need to send a crossed demand draft (DD) drawn in favor of “Director, NIT Warangal” and payable at SBH, NIT Warangal branch

Accommodation

No TA/DA will be paid to the participants. Accommodation will be provided on prior request. Working Lunch, Tea & Snacks would be provided during the training in the campus.

How to apply

A filled-in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator on or before the last date by post/courier. Participants are requested to write their name and contact number backside of demand draft. It is also mandatory to send scanned application form and demand draft through e-mail.

Selection Criteria

Selection will be done based on **first-come-first-serve basis to a** maximum number of 50 (**fifty**). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates

Last date for Submission of Application : 04-11-2016

Selection List Intimation / Display before : 05-11-2016

Duration of program : 8th - 13th November, 2016