About College

Maharai Vijayaram Gajapathi Rai College of Engineering was established in the year 1997 under the aegis of "MANSAS" educational trust. founded by the Raja Saheb of Vizianagaram, Dr. PVG Raju in 1958. The college is located at Vijavaram Nagar campus, Chintalavalasa. Vizianagaram, Andhra Pradesh, The college offers 7 B.Tech. programmes namely EEE, ECE. Mechanical, Civil, CSE, Chemical Engineering and IT & P.G. programmes in Power Systems, Machine Design, VLSI, CSE, Structural Engineering, CNIS and Business Administration. It is accredited with "A" grade by NAAC of UGC. Also UGC has conferred Autonomous status to the college. The college gifted with serene atmosphere amidst green plantation is making its untiring efforts to attain its objective of imparting qualitative technical education. In addition to curricular programs, the institute constantly strives to update its faculty and students by conducting Workshops, Conferences, Seminars and Guest lectures in the emerging fields.

About Department

The Department state-of-art has infrastructure that caters to the outcome based teaching-learning. It has a strong team of qualified & experienced faculty members with a right blend of industry, institution and R&D exposure that supports teaching-learning requirements. In addition to curricular exposure, department strives to keep students aware of the pragmatic needs of the industry for the improvement of employability and professional development. Keeping this in view department has signed MOU with SIEMENS India Pvt. Ltd. to bridge the gap between industry and academia for imparting training to students in the areas of AC/DC Drives, PLC-Automation etc. and it has been equipped with MATLAB, LabVIEW, PSCAD etc.

Theme of the program

The field of Power electronics has been all pervading in its influence especially in the field of renewable and electric vehicle. Power electronics is interdisciplinary in nature and is used in a wide variety of area of Electric Drives, power Quality in power systems and renewable energy systems and electric vehicles etc. This FDP is designed to address applications of power electronics in the industry and to encourage various professionals /students/ academicians towards research and for their academic Quality improvement too. This course will offer a unique opportunity to all the participants in real time power electronic systems and its applications through theoretical sessions and simulation plus laboratory-based experiments and demonstrations.

Apart from that, this FDP will also empower the faculty to be able to make use of the advanced R & D equipment like DSP 28335/28067 kits and also enable them to work on inverter design for a typical solar system.

Objectives of the program

- 1. To enable the faculty to gain insight into cutting edge technologies in the field of power electronic applications
- 2. To empower the faculty to gain hands on exposure in using tools (software and hardware) for simulation studies and experiment based research in the above mentioned field
- 3. To create a forum for ongoing interaction with industry and academia in the form of knowledge transfer for collaborativeresearch



Resource Persons

- 1. Dr. M K Pathak, IIT Roorkee
- 2. Dr. B L Narasimha Raju, NIT Warangal
- 3. Dr. S. G. Kadwane, Nagpur University
- Industry Team: Mr. D. Raveendhra, CTO,
 * Zunik Energies Pvt. Ltd. & 8 members of Zunik Team (including CEO, Tech Leads and Engineers)

*Industry Funded and Supported by TIDES, IIT Roorkee. Zunik Energies (Pre-Incubated startup by IIT Roorkee). www.zunikenergies.com

Convener

Dr. Sarat Kumar Sahu, Professor and Head Co-Conveners

Dr. R. Gowri sankara Rao, Professor

Mr. I. Kranthi Kiran, Associate Professor

Mr. M. Venu Madhav, Associate Professor

Coordinator

Mr. K.S. Ravi Kumar, Associate Professor

Mobile: +91 9182518194 Phone: +91 8922 241167(O)

E-mail: raviks1999@mvgrce.edu.in College website: www.mvgrce.edu.in

Venue

Department of EEE, MVGR College of Engineering. **Registration fee**

Faculty participant Rs. 1000/Industry participant Rs. 1500/Student participant Rs. 500/-

Registration fee is payable through Demand draft drawn in favor of Principal, MVGR College of Engineering, Vizianagaram, on or before Dec 3rd 2017. The registration form can be downloaded from the college web site.

The filled-in form can be sent to mail-ID: percev2017.mvgr@gmail.com

A One-Week

Faculty Development Program

On

Power Electronics Applications for Renewable Energy and Electric Vehicle (PEREEV -2017)

DEC 4th -9th, 2017

REGISTRATION FORM

Participant's Name Designation Department Organization Address Phone no Email- ID **Registration details** DD No . Date: Amount in Rs.: Bank: Signature of the participant Sponsorship certificate Certified that Mr./Ms./Mrs. student/faculty/employee of our institution /industry and is hereby sponsored to attend Faculty Development Program on "Power Electronics Applications for Renewable Energy and Electric Vehicle" at MVGR College of Engineering,

Vizianagaram from 4th December to 9th December

Place:

2017.

Signature of sponsoring Authority with seal

Patron

Sri. P. Ashok Gajapathi Raju Chairman, MANSAS Ms P Aditi Gajapathi Raju Member Trust Board

President

Dr. K.V. L. Raju Principal, MVGR College of Engineering

Advisory Board

Dr. D. R. K Raju, Correspondent, MANSAS Prof. D.J.J.R. Raju, Vice-Principal(Administration)

Dr. Y. M.C Sekhar, Vice-Principal(Academics)

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Dr. K. Rajeswara Rao, Dean (Civil Infrastructure)

Dr. R. Ramesh, Dean (Research & Development)

Dr. S. Srinivasa Rao, Dean (Student Affairs)

Dr. T.V.N. Partha Sarathi, Dean (Examinations)

Dr G.V.S.S.N.Sannyasi Raju, Dean, (Finance)

Organizing Committee

Mr. P Sai Srinivas, Sr. Assistant Professor

Mr. K. Prasada Rao, Assistant Professor

Mr. K.V.V. Prasad, Assistant Professor

Mr. P.A. Mohana Rao, Assistant Professor

Mr. Ch. Bhavani Sankar, Assistant Professor

Mr. P. Pavan Kumar, Assistant Professor

Mr. Ch. Satyanarayana, Assistant Professor

Mr. B. Jagannadh Ch. Yadav, Assistant Professor

Mr. T. Sudhakar, Assistant Professor

Mr. K. Sri Ram, Assistant Professor

Mrs. K. Krishna Kumari, Assistant Professor

Mr. P. Gurumurthy Reddy, Assistant Professor

Mr. S. M.K.Patnaik, Assistant Professor

Mr. G. Shankarmani, Assistant Professor

Mr. D. Ravi Kumar, Assistant Professor

Mr. D. Rajesh, Assistant Professor

Mr J. Venkat Rao, Assistant Professor

Mr. Yoganand Patnaik, Assistant Professor





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Organized by Dept. of Electrical & Electronics Engineering

MVGR College of Engineering (Autonomous)

(Approved by AICTE, New Delhi, Accredited by NBA of AICTE, graded 'A' by NAAC of UGC and Permanently Affiliated to JNTUK)

Chinthalavalasa, Vizianagaram, Andhra Pradesh - 535005

Visit us at: www.mvgrce.edu.in

Mail us to: pereev2017.mvgr@gmail.com