# MANSAS CHUSASTAN MISTIUTIONS

## M V G R COLLEGE OF ENGINEERING(A)

Chintalavalasa, Vizianagaram-535005 Accredited by NAAC with 'A' Grade & Listed u/s 2(f) & 12(B) of UGC (Approved by AICTE, New Delhi and Permanently Affiliated by JNTUK-Kakinada)

Metric No: 6.3.3

## **DVV** Comment:

Provide Event Brochures and reports of (PIPENET 2015 Water Resources of Andhra Pradesh FDP-Real Time applications of Microprocessors and DSP(RTAMD-15) FDP-Soft computing techniques(SCT-2K16) for the year 2015-16,Repair, Rehabilitation and Retrofitting of Reinforced Concrete Elements, RRRRCE-16 FDP-Real Time applications using LabVIEW(RTAL-2K16) "Two Day National Workshop onEmerging Trends in Automotive Technologies for the year 2016-17,FDP-Power Electronic Applications on Renewable Energy and Electrical Vehicles(PEREEV-2K17) Hybrid & Electric Vehicle Technologies (HEVT 2018) "Two Day National Workshop on EDGECAM-2018 "for the year 2017-18,Applications of DGPS in Field Surveying Hydrologic and Hydraulic modeling using MIKE SHE & MIKE HYDRO RIVER FDP-Power Electronic Applications to Power systems(PEAPS-2K18) for the year 2018-19,Water Distribution Network Modeling using EPANET International Conference on Sustainable Systems and Structures Recent trends in Sustainable design of structures NT Staff for the year 2019-20) with Photographs with date and caption.

## **HEI Response**

Event Brochures and reports of all the above FDPs are provided as a single copy in the order mentioned below:

S.No.	Year	Title of the professional development program organised for teaching staff	Page Nos.
1		PIPENET 2015	
2		Water Resources of Andhra Pradesh	
3	2015-16	FDP-Real Time applications of Microprocessors and DSP(RTAMD-15)	01 to 20
4		FDP-Soft computing techniques(SCT-2K16)	
5		Repair, Rehabilitation and Retrofitting of Reinforced Concrete Elements, RRRRCE-16	
6	2016-17	FDP-Real Time applications using LabVIEW(RTAL-2K16)	21 to 44
7		Two Day National Workshop on Emerging Trends in Automotive Technologies	
8	2017.10	FDP-Power Electronic Applications on Renewable Energy and Electrical Vehicles(PEREEV-2K17)	45 4 60
9	2017-18	Hybrid & Electric Vehicle Technologies (HEVT'2018)	45 to 60
10		Two Day National Workshop on EDGECAM-2018	
11		Applications of DGPS in Field Surveying	
12	2018-19	Hydrologic and Hydraulic modeling using MIKE SHE & MIKE HYDRO RIVER	61 to 80
13		FDP-Power Electronic Applications to Power systems(PEAPS-2K18)	
14		Water Distribution Network Modeling using EPANET	
15	2019-20	International Conference on Sustainable Systems and Structures	81 to 83
16		Recent trends in Sustainable design of structures	
17		NT Staff	

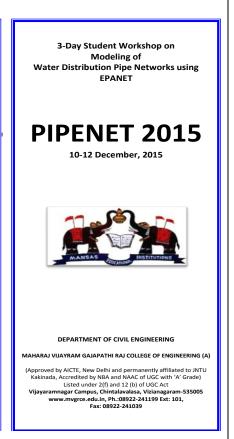
## 2015-16

DEPARTMENT OF CIVIL ENGIEERING			
MVGR COLLEGE OF ENGINEERING (AUTONOMOUS)			
6.3.3 List of Workshops and seminars organised			
AY: 2015-16			
	Descr	2 - 5	0.1

# REPORT ON THREE DAY STUDENT WORKSHOP ON MODELLING OF WATER DISTRIBUTION PIPE NETWORKS USING EPANET (PIPENET 2015) CONDUCTED FROM 10-12-2015 TO 12-12-2015







## 1. ABOUT FDP/ACTIVITY/EVENT

Municipal water distribution systems represent a major portion of the investment in urban infrastructure and a critical component of public works. Nowadays, the demand for freshwater is ever increasing, especially in urban areas. So there is an urgent need for the reliable systems for preserving this resource. The water distribution system includes not only water supply pipe networks but also transmission and treatment. Out of all components, the cost of water supply pipe network is major and is even up to 70% of the total cost. Hence, a water supply engineer is required to know the operational aspects of a water distribution network. For small and simple water distribution networks, it is easy to operate by manual calculations. As the size of the network increases, the complexity increases. Nowadays, the computer technology is progressing like anything and so many computer models are developed for analysis of water distribution networks. EPANET software is one of the popular one for performing analysis based on gradient method. EPANET is a computer program that performs extended period simulation of hydraulic and water quality behaviour within pressurized pipe networks. A network consists of pipes, nodes (pipe junctions), pumps, valves and storage tanks or reservoirs. EPANET tracks the flow of water in each pipe, the pressure at each node, the height of water in each tank, and the concentration of a chemical species throughout the network during a simulation period comprised of multiple time steps. In addition to chemical species, water age and source tracing can also be simulated. EPANET is designed to be a research tool for improving our understanding of the movement and fate of drinking water constituents within distribution systems. It can be used for many different kinds of applications in distribution systems analysis. Sampling program design, hydraulic model calibration, chlorine residual analysis, and consumer exposure assessment are some examples. EPANET can help assess alternative management strategies for improving water quality

throughout a system. These can include: · altering source utilization within multiple source systems, altering pumping and tank filling/emptying schedules, use of satellite treatment, such as re-chlorination at storage tanks, targeted pipe cleaning and replacement.

## 2. RESOUCE PERSONS

- a. Dr. S Chandramouli, Professor, Civil Engineering Department , MVGR College of Engineering
- b. Sri CHV RaviSankar, Asst.Professor,Civil Engineering Department, MVGR College of Engineering

## 3. PROGRAM SCHEDULE

Schedule of the Workshop (Venue: CAAD Lab, Dept. of Civil Engg., MVGR, VZM)

Day-1 (10-11-2015)			
Time	Topics to be covered		
9:00 to 9:30	Registration		
9:30-11:00	Inaugural function including high tea		
	Venue : Admin. Seminar Hall, MVGR		
11:00-1:00	Lecture on Basic principles of analysis of water distribution networks		
1:00-2:00	Lunch		
2:00- 4:00	Overview of Water distribution Systems and Methods of Analysis of		
	Water Distribution Network		
Day-2 (11-11-2015)			
Time	Topics to be covered		
9:30 to 11:15	Analysis of water distribution network using EPANET		
11:15-11:30	Tea Break		
11:30-1:00	Practice session on Analysis of water distribution network using EPANET		
1:00-2:00	Lunch Break		
2:00- 4:00	Practice session on Analysis of water distribution network using EPANET		
Day-3 (12-11-2015)			
9:30 to 11:15	Practice session on Analysis of water distribution network using EPANET		
11:15-11:30	Tea Break		
11:30-1:00	A small Project on modelling of water distribution network using		
	EPANET		
1:00-2:00	Lunch Break		
2:00- 3:00	Evaluation of Project Reports		
3:00-4:00	Valedictory Function		

## 4. LIST OF PARTICIPANTS

External Registration details of PIPENET 2015

S.no	Name of the student	College address				
1	Y SreeApoorva	Shri Vishnu Engg College for Women				
2	Y Charishma	Shri Vishnu Engg College for Women Pa	ge	4	of	83
2	00.					
1 3	G.Srisatya	Shri Vishnu Engg College for Women				

4	D.RaviTeja	AIMS College Amalapuram
5	Md. AmeerJaved	ANITS
6	N.Harish Kumar	ANITS
7	B.Nirosha	GMRIT
8	V VamsiKalyan	Gitam
9	K N Rakesh	Gitam
10	M V Pradeep Kumar	Gitam
11	M Sivateja	Gitam
12	B Renuka	Gitam
13	B SaiCharan Reddy	Gitam
14	B Harish	Gitam
15	B. Lokesh	Gitam
16	O Krishna	Gitam
	JaswanthYadav	
17	K Mahesh vardhan	Gitam
18	Y Aravind	Gitam
19	B Sridhar	Gitam

Department of Civil Engineering MVGR College of Engineering Internal Registration details of PIPENET 2015

S.no	Name of the student	College address
1	Y Prasanthi	MVGR
2	M SaiDeepika	MVGR
3	N Haritha	MVGR
4	K Mounika	MVGR
5	M Manisha	MVGR
6	K Kalavathi	MVGR
7	P Gouthami	MVGR
8	P DivyaRani	MVGR
9	C H Eswar	MVGR
10	J Prudhvi Naidu	MVGR
11	B VamsiNaik	MVGR
12	L Harshavardhan	MVGR
13	ChRaviteja	MVGR
14	G Bharat Kumar	MVGR
15	U Sai Krishna	MVGR
16	V Rajesh	MVGR
17	A. Bhavana	MVGR
18	K.Harshasri	MVGR
19	G N M Lavanya	MVGR
20	B Pallavi	MVGR
21	K R S Kiran	MVGR
22	K PremKumar	MVGR
23	A AsifBaig	MVGR
16	V Rajesh	MVGR
17	B. Bhavana	MVGR
18	K.Harshasri	MVGR
19	G N M Lavanya	MVGR
20	B Pallavi	MVGR
21	K R S Kiran	MVGR

22	K PremKumar	MVGR
23	A AsifBaig	MVGR
24	K.Bhavagnya	MVGR
25	S.MadhusuddanRedy	MVGR
26	SatishVanapalli	MVGR
27	S.Likith	MVGR
28	B.Pavani	MVGR
29	AdasadaManjari	MVGR
30	DubaMounica	MVGR
31	C.Harshasree	MVGR

## **5. PHOTOGRAPHS**





## **About College**

Maharaj Vijayaram Gajapathi Raj College of Engineering was established in the year 1997 under the aegis of educational trust "MANSAS", founded in 1958by the Raja Saheb of Vizianagaram popularly known as PVG. The college is located at Vijayaram Nagar campus, Chintalavalasa, Vizianagaram, Andhra Pradesh which is 1 hour drive from Visakhapatnam the city of destiny. The college offers 7 B.Tech programmes, namely, EEE, ECE, Mech, Civil, CSE, Chemical Engg and IT & P.G programmes in Machine Design, Structural Engineering, VLSI, CSE, CNIS, Power Systems, Computer applications and Business Administration. It is accredited with 'A' grade by NAAC of UGC. Also UGC has conferred Autonomous status to the college. The college gifted with serene atmosphere amidst green plantation is making its untiring efforts to attain its objective of imparting qualitative technical education. The institute in addition to curricular programs constantly strives to update its faculty and students by conducting Workshops, Conferences, Seminars and Guest lectures in the emerging fields.

## **About Department**

The Department has state of the art infrastructure that caters to the outcome based teaching learning. A strong team of qualified & experienced faculty members with a right blend of industry, institution and R&D exposure supports teaching-learning requirements. In addition to curricular exposure department strives to keep students aware of the pragmatic needs of the industry for the improvement of employability and professional development. Therefore, department has signed MOU with Siemens India Pvt. Ltd. and New Dawn Automation to bridge the gap between industry and academia for imparting training to students in the areas of PLC, Drives, Automation, MATLAB, LAB VIEW etc.

### Objective of the workshop

Faculty will be able to use Micro controller for control of electrical machines. They will gain hands on experience.

#### **Course Contents:**

- Beginner's Introductory for Programming AVR
- AVR Family of Microcontrollers
- Embedded 'C' Programming
- LCD & ADC Interfacing and general purpose input-output
- PWM Generation, Hardware Interfacing & Applications

- Introduction to DSP Processors,
   TMS320F28335 for Control and Power
   Electronic based Application
- Advanced Application: Multilevel Inverter

## **Resource Persons**

Dr. D.K.Mohanta, MVGRCE(A), Vizianagaram

Dr. S.G. Kadwane, YCCE, Nagpur

Dr. M.J.B. Reddy, NITTrichy

#### Date & Venue

Date :June15<sup>th</sup> - 19<sup>th</sup>, 2015 Venue :Department of EEE

MVGR College of Engineering, Vizianagaram, Andhra Pradesh.

## Eligibility for participation

Microcontrollers have found niche as important constituents for most of the gadgets such as remote controllers, audio/video devices, mobiles etc. So, Faculty Members/ Practicing Engineers/ Scientists/ Research Scholars from any disciplines are invited.

### **Registration Fee**

Category	IEEE	Non-IEEE
	Members	Members
Faculty	Rs.2000/-	Rs.2500/-
Student	Rs.1000/-	Rs.1500/-
Industry	Rs.3000/-	Rs.3500/-

Registration fee is payable through Demand draft in **favour of Principal, MVGR College of Engineering, Vizianagaram** payable at **Vizianagaram**, on or before 12<sup>th</sup>June2015. The registration fee includes kit cost as well as working lunch. The registration form can be downloaded from the college website. The duly filled form can be sent to the following address.

## Address for Communication: Coordinator:

Mr. M. Venu Madhav Associate Professor EEE Department

MVGR College of Engineering Chintalavalasa, Vizianagaram Mobile:+91-9849403410 Phone: +91 8922-241167(O) E-mail: vmmvilli@gmail.com

College website: www.mvgrce.edu.in

# A Faculty Development Programme on Real-time Applications of Micro- controllers &DSP

June 15<sup>th</sup> - 19<sup>th</sup>, 2015





## Technically Sponsored by IEEE Vizag Bay Sub-Section Registration form

Participant's Name
Designation
Department
Organization
Address for
Communication
Ph.No
E-mail ID

: 

Designation
: 
Participant's Name
: 
Pearticipant's Name
:

## **Registration Details**

DD No. : Date: Amount Rs. : Bank:

Signature

#### SPONSORSHIP CERTIFICATE

Certified that Mr./Ms	is a regular
employee of our institution since	_and is hereb
sponsored to attend a faculty Development	Programme or
Real-Time Applications of Micro-controllers	& DSP at
MVGR College of Engineering, Vizianagara	m during
15 <sup>th</sup> –19 <sup>th</sup> June 2015.	

Place:

#### **Chief Patron**

Dr. P.Anand Gajapathi Raju Chairman, MANSAS

#### **Patron**

Sri. P.Ashok Gajapathi Raju Vice- Chairman, MANSAS

#### **President**

Dr. K.V.L.Raju Principal

### **Advisory Committee**

Dr. A. Raghava Rao Correspondent, MANSAS.

Prof. D.J.J. Ramachandra Raju Vice-Principal (Admn)

Dr. D.R. Prasada Raju Dean (Faculty Development)

Dr. P. Ravindranadh Dean (Strategic Planning)

Dr. Y.M.C. Sekhar Vice-Principal (Academics)

Dr. R.Ramesh Dean (R&D)

## **Technical Program Committee**

Dr. A.K. Pradhan IIT Kharagpur

Dr. B. K. Panigrahi IIT Delhi

Mr. K.Ramakrishna IEEE Region 10

(Asia - Pacific) Director

Dr. Sivaji Chakrvorti IEEE India Council

Chair Elect

Dr.H.M. Suryawansi VNIT Nagpur

Dr. M. J. B. Reddy NIT Trichy

Dr. C. K. Panigrahi KIIT University Bhubaneswar

Dr. D.K.Mohanta MVGRCE VZM

Dr. S. G. Kadwane YCCE Nagpur

Dr. R. Ramana Reddy IEEE Student Branch

Counselor, MVGRCE

#### Convener

Dr.D.K.Mohanta

Professor & Dean (Collaborative Research)

Head of the Department

#### **Co-Conveners**

Dr. R.Gowrisankara Rao Professor, EEE Department

Dr. Sarat Kumar Sahu

Professor, EEE Department

#### Coordinator

Mr. M.Venu Madhav

Assoc. Professor, EEE Department

## **Co-Coordinator**

Mr. P.Sai Srinivas

Sr. Asst. Professor, EEE Department

### **Organizing Committee:**

Mr. K.S.Ravi Kumar, Assoc. Prof. EEE

Mr. I.Kranthi Kiran, Assoc. Prof. EEE

Mr. N.Tejeswara Rao, Sr. Asst.Prof. EEE

Mr. P.A.Mohana Rao, Asst.Prof. EEE

Mr. Ch. Bhavani Sankar, Asst. Prof. EEE

Mr. P.Pavan Kumar, Asst. Prof. EEE

Mr. Ch. Satyanarayana, Asst. Prof. EEE

Mr. B.Jagannath Ch. Yadav, Asst. Prof. EEE

Mr. T.Sudhakar, Asst. Prof. EEE

Mr. N.S.S.S.Chandra, Asst. Prof. EEE

Mr. K. Sri Ram, Asst. Prof. EEE

Ms. J.Sri Sandhva. Asst. Prof. EEE

## A Faculty Development Programme

on

# Real-time Applications of Micro-controllers & DSP

June 15<sup>th</sup> – 19<sup>th</sup>, 2015





#### Technically Sponsored by IEEE Vizag Bay Sub-Section



Organized by

# Department of Electrical & Electronics Engg. MVGR College of Engineering (Autonomous)

(Listed Under 2(f), 12(b) Act of UGC)
(Permanently Affiliated to JNTU, Kakinada, and Approved by AICTE & NBA, New

Delhi, Accredited with "A" grade by NAAC)

## Chintalavalasa, Vizianagaram Andhra Pradesh, India

College website: www.mvgrce.edu.in

## A FACULTY DEVELOPMENT PROGRAM

on

# REAL-TIME APPLICATIONS OF MICRO-CONTROLLERS &DSP (RTAMD-15)

Technically Co-sponsored by IEEE Vizag Bay Sub Section
Organized by

# Dept. of Electrical and Electronics Engineering MVGR College of Engineering(Autonomous), Vizianagaram, INDIA (15th - 19th June 2015)

A Faculty Development Program (FDP) on applications of Micro-Controllers & DSP was organized successfully during 15<sup>th</sup>-19<sup>th</sup> June, 2015 by the EEE department of MVGR College of Engineering, Vizianagaram and was technically co-sponsored by IEEE Vizag Bay Subsection. Dr. K. V. L. Raju, Principal and Prof. D. J. J. Ramachandra Raju, Vice-Principal (Administration) of MVGR College of Engineering have been very keen to offer hands-on skills to faculty members in the domain of cutting-edge technologies to train students effectively to bridge gap between academia and industry. Since Microcontroller and Digital Signal Processors have found niche as important constituents in the fast growing digital world, the FDP was well-attended by 43 participants from different institutions. Dr. Lakshminarayana, Chairman as well as Mr. T. S. N. Murthy, Secretary & Treasurer, IEEE Vizag Bay Subsection have conferred technical co-sponsorship on behalf of IEEE tofoster the growth of skill and knowledge in electrical and electronics engineering amongst the technical communities.

The inaugural chief guest was Dr. Sivaji Chakravorti, Professor, Jadavpur University, Kolkata and Chairman-Elect of IEEE India Council. He emphasised the importance of such FDP's to assist life-long learning skills. He delivered a very conceptual keynote address on 'Estimation of instantaneous frequency of a signal' which kept the audience captivated by his cogent and lucid presentation from the perspective of applications in Microcontrollers and Digital Signal Processors.

Dr K V L Raju, Principal deliberated on importance of microcontrollers and DSP processors in day to day life keeping in view their rapid growth in development and research applications.







Dr S G Kadwane, Professor, YCCE-Nagpur was the guest of honour as well as one of the resource persons for the workshop He had shared his views on careful listening, interaction, application of thought and hands on skills for becoming competent professional.

Dr D K Mohanta, Convener and one of the resource persons of the program presented the overview as well as vision of the program to inculcate hands on skills to faculty members so as to deliver effectively to the students.



Dr R. Ramana Reddy, Branch counsellor, IEEE students' branch, MVGR College of Engineering, briefed about activities organized byIEEE students' branch in the college. He thanked IEEE Vizag Bay Sub section for technical sponsorship of this program.

## **Day 1:**

First session was conducted by Prof Kadwane. He started with Overview on AVR Family of Microcontrollers.

During second session, hands on session wereon fabricating of programmer for Microcontrollersusing discrete components.



## *Day 2:*

During first session Prof D K Mohanta delivered a lecture on fundamentals of DSP for real-time digital signal processing. He also gave an exposition regarding applications of DSP controllers.

During the second session, Prof S G Kadwane deliberated on 'Beginner's Introductory for Programming forAVR using Embedded 'C'. The practical session was regarding dumping of the program and their successful execution.



## *Day 3:*

During the first session, Prof. D K Mohanta delivered a lecture on 'Introduction to DSP Processors' and on TMS320F28335 for Control and Power Electronic based applications.

During the second session, Prof S G Kadwane explained about interfacing of LCD & ADC as well as general purpose input-output and then he conducted hands-on session .



## *Day 4:*

During first session, Prof S G Kadwane explained about PWM, hardware Interfacing &different applications.

During the second session, Dr. M.J.B. Reddy, NIT Trichy elaborated on architecture of TMS Processors. He also explained various types of DSP processors and their applications.

During last session, hands-on session practice was imparted regarding generation of PWM and hardware interfacing.



## *Day 5:*

During the first session, Prof S. G. Kadwane explained about advanced application of multi-level inverter in anexhaustive manner and it was followed by a hands-on session.

The workshop concluded with a valedictory function and chief guest for the occasion was Dr. A Raghava Rao, Correspondent, MANSAS Trust. Prof. D.J.J. Ramachandra Raju, Vice-principal (Administration) as well as Professor of the department expressed about the reflection of learning from FDP through continued application of skills in different pragmatic ways. Participants have highlighted feedback that the workshop was really very useful to acquire practical knowledge about Micro controllers and DSP and urged to organize another advanced level follow up program within couple of months. Certificates of participation to delegates were awarded by the chief guest as well byProf. Ramachandra Raju.



The co-convenors, Dr. R. Gowrisankar Rao and Dr. Sarat Kumar Sahu have extended their full cooperation for effectively conducting the event. The co-ordinator, Mr. M. Venu Madhav had meticulously planned and executed all the sessions assisted by Mr. P. Sai Srinivas. All the faculty members of the of EEE Department have whole-heartedly extended their cooperation with motivation to utilize the acquired practical skills. The management of the institute, especially the Principal and Vice-Principal (Admn.) have been very generous to extend all needed support out of genuine spirit of being faculty members in spite of busy schedules.

## About FDP/ event/ activity

b. Resource persons Dr D K Mohanta,Professor,BIT Mesra,ranchi Dr M Jayabharatha Reddy,Asscoiate professor, NIT Trichy Dr S G Kadwane ,Professor,Nagpur University

c. Program schedule

## **Program Schedule**

Day	Time	Event
	9:30- 10:45	Inaugural session
	10:45-11:00	Tea Break
	11:00-01:00	Key note address by Dr. Shivaji Chakravorti, Professor, Jadavpur
Monday		University, Kolkata & IEEE India Council Chair Elect
15-06-2015	01:00-02:00	Lunch Break
	02:00-03:00	"Overview on AVR Family of Microcontrollers" by Dr. S.G. Kadwane
	03:00-03:15	Tea Break
	03:15-04:30	Practice Session
	9:30- 11:00	"Fundamentals of DSP for Real Time Processing" by
		Dr. D.K. Mohanta
Tuesday	11:00-11:15	Tea Break
16-06-2015	11:15-01:00	"Beginner's Introductory for Programming AVR; Embedded 'C'
10-00-2013		Programming" by Dr. S.G. Kadwane
	01:00-02:00	Lunch Break
	02:00-04:30	Practice Session
	9:30- 11:00	"Introduction to DSP Processors, TMS320F28335 for Control and Power
		Electronic Based Application" by Dr. D.K. Mohanta
Wednesday	11:00-11:15	Tea Break
17-06-2015	11:15-01:00	"LCD & ADC Interfacing and General purpose input-output" by
1. 00 2010		Dr. S.G. Kadwane
	01:00-02:00	Lunch Break
	02:00-04:30	Practice Session
	0.20 11.00	(DND 4 C
Thursday Dr. S.G. Kadwane		"PWM Generation, Hardware Interfacing & Applications" by
18-06-2015	Tea Break	
	11:15-01:00	"Architecture of TMS Processors" by Dr. M.J.B. Reddy

	01:00-02:00	Lunch Break
	02:00-04:30	Practice Session
	9:30- 10:30	"Advanced Application: Multi Level Inverter" by Dr. S.G. Kadwane
Friday	10:30-10:45	Tea Break
19-06-2015	10:45-01:00	Practice Session
	02:00-03:00	Valedictory Function

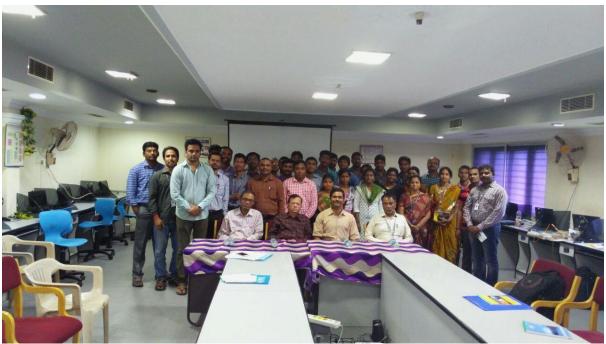
### d. List of participants

- 21.Mr Krishnam Naidu, Assistant Professor, NSRIT, A.P.
- 22. Sowjanya , Mtech ,PS,MVGR College of Engineering, Vizianagaram
- 23. M Pradeep Chandra Varma ,Mtech ,PS,MVGR College of Engineering,Vizianagaram
- 24 .Sateesh, Mtech ,PS,MVGR College of Engineering,Vizianagaram
- 25. P S Vasavi, Mtech ,PS,MVGR College of Engineering, Vizianagaram
- 26. Muddada Venkatesh, Mtech ,PS,MVGR College of Engineering,Vizianagaram
- 27. Bonula Sowjanya Kalyani, Mtech ,PS,MVGR College of Engineering, Vizianagaram
- 28. Tippanu Venkata Divya Jyothi, Mtech ,PS,MVGR College of Engineering, Vizianagaram
- 29.Y Jaswanth, Mtech ,PS,MVGR College of Engineering, Vizianagaram 30.

## e. Photographs







# Two Day National Workshop on Soft Computing Techniques(SCT-2K16)

## Theme of the Workshop:

Soft computing aims at imitating the human mind's ability to effectively utilize approximate rather than exact modes of reasoning to enhance machine intelligence. Soft Computing techniques incorporate imprecision, uncertainty and partial truths unlike traditional computing techniques aiming at precision, certainty and rigour. Soft Computing Techniques such as evolutionary programming, genetic algorithm, fuzzy logic, neural network etc, have been extensively used for engineering and other highly complex dynamic systems. This workshop aims to provide a forum for exposition of state —of- the art for different applications.

## **Objective of the Workshop:**

The objective of the workshop is to acquaint the participants with soft computational techniques for different applications. This workshop has been organised by EEE Department in association with the Indian Society for Technical Education student chapter to bring together reputed national level experts to exchange knowledge in this field for different applications, evolutionary programming techniques. The workshop will benefit for students, faculty and researchers.

## **Resource Persons:**

Dr.B.K.Panigrahi IIT Delhi Dr.Swagatam Das ISI Kolkata

Dr.D.K.Mohanta MVGRCE, Vizianagaram Dr.M.J.B. Reddy NIT Trichy

## **Conveners**

Dr. D.K.Mohanta Professor & Head of EEE

Mr. I. Kranthi Kiran Advisor, ISTE Student Chapter

## **Coordinators**

Dr.R. Gowri Sankara Rao Professor Dr. Sarat Kumar Sahu Professor

## **Student Coordinators:**

Mr. B.Charan Kumar Mr. G.Bhagya Pavan Mr. G.Navaneeth

## Date & Venue:

14<sup>th</sup> & 15<sup>th</sup> March, 2016 at Department of EEE

## **Registration Fee:**

Student Participant Rs. 250 Faculty Participant Rs. 500

## **PROGRAMME SCHEDULE**

Date	Sessions	Time	Activity	Resource Person
	Inaugural Session	10.30a.m– 11.30a.m	Inauguration	Dr. Sri Ram Srinivas, Associate
	Keynote Lecture	11.45a.m- 1.00p.m	Keynote Lecture	Professor, IIT Chennai as a chief guest
14 <sup>th</sup> March, 2016	Session – I	2.00p.m –3.45 p.m	Soft Computing Techniques for Electrical Engineering Applications	Dr. Sri Ram Srinivas, Associate Professor, IIT Chennai
	Session – II	4.00 p.m- 6.00 pm	Research Methodology using Soft Computing Techniques	Dr. Swagatam Das, Indian Statistical Institute Kolkata,
15 <sup>th</sup> March	Session – III	9.30a.m– 11.30a.m	Evolutionary Competition and	Dr. Swagatam Das, Indian
2016	Session – IV	11.45a.m- 1.30p.m	Competition and machine learning	Statistical Institute, Kolkata,

	Lunch	1.30 p.m – 2.30 p.m
	Veldictory	2.30 p.m to 3.30 p.m
	function	2.50 p.m to 5.50 p.m



14<sup>th</sup> March, 2016: The inaugural sesssion and keynote address was given by ,Dr. Sri Ram Srinivas, Associate Professor, IIT Chennai as a chief guest.



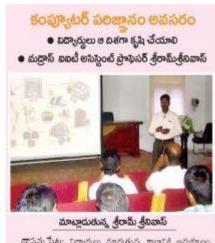
## సాఫ్ట్ కంప్యూటింగ్త్ బహుక ప్రయోజనాలు



## మాట్లాడుతున్న అసిస్టెంట్ ప్రాఫెసర్ శ్రీరాం శ్రీనివాస్

ఎంపీజీఆర్ క్యాంపస్(డెంకాడ): పవర్ ఎల్మక్టానిక్స్, మైక్రో -గ్రిడ్ రంగాల్లో ఉత్పన్నమయ్యే పలు సమస్యలను సాఫ్ట్ కంప్యూటింగ్ పద్ధతుల ద్వారా సులువుగా పరిష్కరించవచ్చని మద్రాస్ ఐఐటీ ఎలక్షికల్ ఇంజినీరింగ్ అసిస్టెంట్ ప్రాఫేసర్ శ్రీరాం శ్రీనివాస్ అన్నారు. మంగళవారం ఎంపీజీఆర్ ఇంజనీరింగ్ కళాశాలలో ఎల్మక్రికల్ ఇంజీనీరింగ్ విభాగం, కళాశాల ఐఎస్టీటీఈ విద్యార్థి సంఘం సంయుక్తంగా సాఫ్ట్ కంప్యూటింగ్ పద్దతులపై జాతీయ సదస్సు నిర్వహించారు. ఈ సందర్భంగా ఆయన మాట్లాడుతూ విద్యుత్ అవసరాలు, వినియోగంపై సాఫ్ట్ కంప్యూటింగ్ ద్వారా తెలుసుకోవచ్చన్నారు. విద్యుత్ సమస్యలను పరిష్కరించవచ్చని చెప్పారు. కోల్కతా ఐఎస్ఐ ప్రాఫెసర్ దాస్ మాట్లాడుతూ వైద్యం, కంప్యూటర్, మెకానికల్, సివిల్, కమ్యూనికేషన్స్, ఎల్మక్రికల్ వంటి రంగాల్లో కంప్యూటింగ్ ఉపయెగాలు, విధివిధానాలు వివరించారు. కళా శాల ప్రిన్ఫిపాల్ డాక్టర్ కేవీఎల్ రాజు మాట్లాడుతూ కంప్యూటింగ్ పద్దతు లను అనుసరించి వృత్తివరమైన సమస్యలకు సమాధానం పొందవేచ్చ న్నారు. కార్యక్రమంలో ఎల్మక్టికల్ విభాగాధిపతి డి.కె.మహంతి, మాన్సాస్ కరస్పాండెంట్ ఎ.రాఘవరావు, వైస్ ప్రిస్సిపాల్ డి.జె.జె.రామచంద్ర రాజు, అధ్యాపకులు పాల్గొన్నారు.





దాపన్న పేట: ఏద్యార్తులు మారుతున్న కాలానికి అనుకూలం గా కంప్యూటర్ పరిజ్ఞానాన్ని పెంపొందిందుకోవాలని మద్దాన్ ఇంటీ అసిస్టెంట్ ప్రొఫెనర్ డాక్టర్ గ్రీరామ్ట్రీనివాన్ సూచించారు. మంగళవారం చింతలవలసలోని ఎంచీజీఆర్ ఇంజిసీరింగ్ కళాశాలలో ఎలక్షికల్ ఇంజిసీరింగ్, ఇఎస్టేటీ ఈ విద్యార్థ సంఘం నుందు క్షంగా సాఫ్ట్ కంప్యూటింగ్ పద్ధకులు అనే అంకంపై జాతీయ స దమ్మ నిర్వహించారు. ఈ సందర్భంగా అయన మాట్లుదుతా ఎలక్షికల్ ఇంజిసీరింగ్ విద్యార్థులకు ఈ పద్ధకులు ఉపయుక్త మన్నారు. వీటిని అమోతం చేసుకుంటే ఉత్పల భవిష్యత్తు ఉంటుందన్నారు. కళాశాల ఫిస్మిపాల్ డాక్టర్ కేమీల్ రాజు మాట్లుదుతూ ఈ సదస్సూరు, ఇళ్ళారు మిర్మానునులను కళాశాల ఎంట్మికల్ ఇళ్లు వరిస్సారం కరిసింగ్ విజ్ఞాగునుకి చాక్షరు వీరే ముహంతా, విద్యాసుస్తుల కరిసింగ్ విజ్ఞాగునుకి చాక్షరు వీకే ముహంతా, విద్యాసుస్తుల కరిసింగ్ విజ్ఞాగునికి చాక్షరు వీరే సిన్మిసినాల్ రామచంద్రవరాజు, డాక్టరు స్వాగత దాను, ఎద్యార్మలు పాల్గొన్నారు.

About 40 participants have attended the workshop and got benefitted.

## 2016-17

## REPORT ON TWO DAY NATIONAL LEVEL SEMINAR ON REPAIR, RAHABILITATION AND RETROFITTING OF REINFORCED CONCRETE **ELEMENTS**

## CONDUCTED FROM 21-10-2016 TO 22-10-2016

## ORGANIZING COMMITTEE

Chairman-MANSAS

Principal, MVGR

Correspondent, MANSAS

Professor, Civil, MVGR

Vice-Principal, Admin., MVGR

Professor & Dean C&I, MVGR

Prof. & HOD, Civil Engg, MVGR

Professor, Civil Engg, MVGR

Asst. Prof., Civil Engg. MVGR

Vice-Principal, Academics, MVGR

CHIEF PATRON

Dr. P Ashok Gajapathi Raju

PATRON

Dr. KVL Raju

ADVISORY BODY Dr. DRK Raju

Prof. DJJ Ramachandra Raju Dr. YMC Sekhar Dr. K Rajeswara Rao

Dr D Prasada Raju Dr. K Suryanarayana

CONVENOR Dr. S Chandramouli

ORGANISING SECRETARY

Dr. P Markandeya Raju

FACULTY COORDINATOR Sri K Santosh Kumar

ORGANIZING BODY

Associate Professors
Dr. R Maheswaran

Sri B Ramesh Raju

Dr. Partheepan Ganesan

Assistant Professors Sri S Murali Sagar Varma Sri V Vinay

Sri A Varaprasad Sri Ch V Ravi Sankar Sri Rajendra Prasad Singh Sri P Sudheer Sri B Ramu Sri BV Joga Rao

Sri S Purushotham Rao Sri TP Sreejani Sri Kalyan AVS Sri SS Bhanu Sai Kumar Sri K Anil Pradeep Sri W Sai Deepak Sri T Jahnavi Sri M Amulya

Sri M Niranjan Bhatlu

Sri K Venkateswara Rao

Smt D Praseeda

FOR DETAILS/ CLARIFICATIONS, PLEASE CONTACT

Dr. P MARKANDEYA RAJU ORGANISING SECRETARY (91) 94405 28043

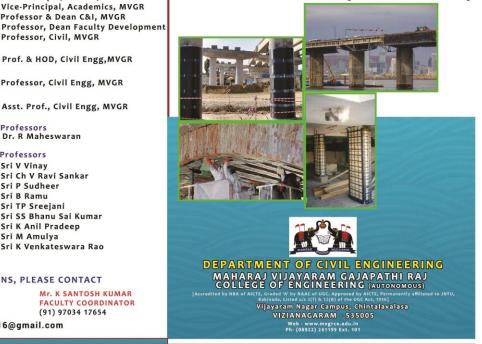
Mr. K SANTOSH KUMAR FACULTY COORDINATOR (91) 97034 17654

Email - rrrrce16@gmail.com

## Two Day National Level Seminar on

REPAIR, REHABILITATION AND RETROFITTING OF REINFORCED CONCRETE ELEMENTS

[21-22 October, 2016]



## **ABOUT MVGR**

ABOUT MVGR

Maharaj Vijayaram Gajapathi Raj College of Engineering (Autonomous) was established in the year 1997 under the aegis of educational trust "MANSAS" (Maharaj Alak Narayana Society for Arts and Sciences) founded by Late Dr. PVG Raju, Raja Saheb of Vizianagaram. The college is located at Chintalavalasa, Vizianagaram, 45min drive from Visakhapatnam and 10min drive from Vizianagaram. The college offers 7 B.Tech and 7 P.G programmes (M.Tech and MBA) in Machine Design, Structural Engineering, VLSI, CSE, CNIS, Power Systems and Business Administration. The college is gifted with serene atmosphere amidst green plantation. The institute is putting untiring efforts to attain the objective of imparting quality technical education . The institute in addition to curricular programs constantly strives to update its faculty and students with emerging and most recent developments in various disciplines through industry-academy interactions by conducting International and National Conferences, Workshops, Seminars, Add-on Courses and Guest Lectures. The institution is in collaboration with PTC, siemens, WIPRO, TCS, Oracle, Ni lab View, IIRS, CISCO and other reputed national level organizations offering additional skill development program leading to certifications.



### ABOUT THE DEPARTMENT

Department of Civil Engineering, established in the year 2009 offers UG (B.Tech) in Civil Engineering and PG (M.Tech) in Structural Engineering. The Department has state of the art infrastructure and well equipped laboratories. 28 well qualified strong team of faculty with a blend of industry, R&D and teaching experiance with varied specializations offer teaching-learning services. Among which 6 are doctorates, 21 are Post Graduates and 5 faculty members are pursuing their Doctoral research. The department has a dedicated Computer Centre & Research Doctoral research. The department has a dedicated Computer Centre & Research Centre promoting active research. The department has certified competency for service development leading to award of certification in remote sensing & STAAD Pro in collaboration with IIRS and Bentley. The faculty of the department have published more than 100 high quality research papers in Journals of International Repute. The department also offers consultancy services to various Government and Private organizations like GVMC, Vizianagaram Traffic Police, AP Road Safety Department, CPWD, Rural Water Supply, Visakhapatnam Urban Development Authority, SPCB Vizianagaram Traffic Police. Vizianagaram



An advanced course on "REPAIR, REHABILITATION AND RETROFITTING OF REINFORCED CONCRETE ELEMENTS" is being conducted by Department of Civil Engineering, MVGR College of Engineering (Autonomous), Vizianagaram from 21st - 22nd

Distress of RC elements due to various unavoidable factors is a serious challenge being faced by Structural Engineers today as it seriously affects the performance and safety of the structure as whole. Quite often, it is not easy to foresee the implication of environment and improper quality control during concrete construction that leads to distress and deterioration. Structural problems in deteriorated systems, aging, lack of durability, and undesirable structural responses are matters of continuous research. Investigations are being carried out globally to improve both strength and serviceability of the RC structures using appropriate techniques of repair, rehabilitation and retrofitting. Retrofitting of concrete structures to meet the more stringent limits on serviceability and ultimate strength as per current Codal requirements seems to be a more attractive alternative to demolishing and rebuilding. Repair of structures becomes inevitable and it is necessary to estimate the degree of damage before choosing an appropriate repair method. In today's construction scenario, availability of materials for repair rehabilitation and retrofitting of RC Structural elements has become a major challenge.

In view of these aspects, A Two Day National Level Seminar is being organised to discuss some of the issues in this area. This advanced course is intended to provide a better and well directed exposure to the current research and development, practices and emerging trends in areas of repair, rehabilitation & retrofitting of RC members.

This seminar throws light on the following areas

Introduction to Repair, Rehabilitation and Retrofitting of RC Elements Repair and Retrofit philosophy: materials and methodologies

Condition Assessment using NDT Shear and Flexure Rehabilitation of RC beams

Rehabilitation of RC elements with fibre reinforced polymer materials Case studies on Rehabilitation of RC Structural Elements

## External prestressing for Retrofitting of RC Beams **RESOURCE PERSONS**

Dr. D RAMASESHU Professor & HoD, Civil Engineering National Institute of Technology Warangal

Dr. V RAVINDRA
Professor, Civil Engineering
JNTUK College of Engineering,

Dr. P MARKANDEYA RAJU Professor, Civil Engineering MVGR College of Engineering Dr. DSR MURTY
Professor & Chairman BoS
Civil Engineering
College of Engineering, AU, VSP

Dr. AMIRTHAM RAJAGOPAL Associate Professor Civil Engineering IIT Hyderabad

Dr. PARTHEEPAN GANESAN
Associate Professor, Civil Engineering
MVGR College of Engineering

## 1. ABOUT ACTIVITY:

Distress of REC elements due to various unavoidable factors is a serious challenge being faced by the Structural Engineers today as it seriously affects the performance and safety of the structure as whole. Quite often, it is not easy to foresee the implication of environment and improper quality control during concrete construction that leads distress and deterioration. Structural problems in deteriorated systems, aging, lack of durability and undesirable structural responses are matters of continuous research. Investigations are being carried out globally to improve both strength and serviceability of the RC structures using appropriate techniques of repair, rehabilitation and retrofitting. Retrofitting of concrete structures to meet the more stringent limits on serviceability and ultimate strength as per the current Codalrequirements seems to be a more attractive alternative to demolishing and rebuilding. Repair of structures becomes inevitable and it is necessary to estimate the degree of damage before choosing an appropriate repair method. In today's construction scenario, availability of materials for repair, rehabilitation and retrofitting of RC Structural elements has become a major challenge.

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## 2. RESOURCE PERSONS:

- a. Dr.G V Appa Rao, IIT Madras
- b. Dr.A Rajagopal, IIT Hyderabad
- c. Dr.Gunneswara Rao, NIT Warangal
- d. Dr. V Ravindra, Professor, JNTUK
- e. Dr.DSR Murthy, Professor, AU
- f. Dr.P.Markandeya Raju, Professor, MVGR College of Engineering
- g. Dr.PartheepanGaneshan, Assoc.Professor, MVGR college of Engineering
- h. M/s FOSROC/BASF/SIKA/Dr.FIXIT CHEMICALS

## 3. PROGRAM SCHEDULE

Day-1	
TIME	EVENT
8.30 -9.30	Registration
9.30-10.30	Inaugural function
	including high tea
10.30-12.30	Lecture 1
12.30-1.30	Lunch
1.30-3.30	Lecture 2
3.30-4.30	NDT Equipment and
	Constrction Chemicals
	Demonstration
4.30-5.00	Tea
Day-2	
TIME	TOPICS TO BE
	COVERED
9.15-11.15	Lecture 3
11.15-11.30	Tea Break
11.30-1.00	Lecture 4
1.00-2.00	Lunch Break
2.00-3.30	Lecture 5

3.30-4.00	Evaluation
4.00-4.30	Valedictory Function
4.30-5.00	High Tea

## DEPARTMENT OF CIVIL ENGINEERING M V G R COLLEGE OF ENGINEERING (AUTONOMOUS)

TWO DAY NATIONAL SEMINAR ON "REPAIR, REHABILITATION AND RETROFITTING OF REINFORCED CONCRETE ELEMENTS" (RRRRCE16) on 21.10.2016

## SCHEDULE OF THE PROGRAM

TIME/DURATION	EVENT
9:30AM To 10: 25AM	Inaugural Function
9:30 To 9:32	Inviting Guests on to the Dias
9:32 Te 9:33	Lighting the Lamp by Dignitaries
9:33 To 9:35	Prayer Song
9:35 Tc 9:40	Address by the organizing scordary
9:40 Fe 9:45	Inaugural Address by Convener
9:45 To 09:50	Message by Correspondent
09.50 Tu 09.55	Message by Principal
09.55 To 09.57	Introduction to Guest of Honour
09:57 To 10:02	Message by the Guest of Hondur
10:02 To 10:10	Introduction to Chief Guest
10.10 To 10.15	Message by Chief Guest
10:15 To 10:20	Memerics to the Guests by the Correspondent & Principal
10.20 To 10.25	Vote of Thanks
10:25 To 10:45	High Ten Brenk

## DEPARTMENT OF CIVIL ENGINEERING M V G R COLLEGE OF ENGINEERING (AUTONOMOUS)

TWO DAY NATIONAL SEMINAR ON "REPAIR, REHABILITATION AND RETROFITTING OF REINFORCED CONCRETE ELEMENTS" (RRRRCE16) on 21.10.2016 & 22.10.2016

## VALEDICTORY SCHEDULE OF THE PROGRAM

netion ies on to the D'as
ies on to the Dias
y Participants
by Organizing Secretary, Dr.P M.Raju
vener & HoD, Dr.S.Chandramouli
e-Principal(Acdomics), Dr. Y M C Sekhar
ef Guest, Dr.D. Ramascahu
tertificates to Participants
ov Mr.K. Santhosh Kumar, Faculty Contdinator
-

## LIST OF PARTICIPANTS

# DEPARTMENT OF CIVIL ENGINEERING MVGR COLLEGE OF CIVIL ENGINEERING(A)

TWO Days National Seminar on Repair, Rehabilitation and Retrofitting of Reinforced concrete (RRRPCE-18) on 21st and 22nd October, 2018
Régistration Form

No.	WAME OF THE PARTICIPAN	ORGANIZATION	Fee Par
1	ABDU_RASHID	CAP COLLEGE DE ENGINEER WO	
2	NAD J BOTELA	SVF COLLEGE OF ENGINEERING	533
3	ALOKESWARA RAD	LOYAPLE CEMENT CORPORATION	500
6	V SATYANARAYANA MURTHY	ADDE VISAKHAPATNAM	300
0	PDL 84I WANCEAR	MAN ASSOCIATES	800
c	NISHANTH	MINE ASSOCIATES	500
7	AAY KUMAR	AMRUTE	500
9	2 KAISHAAM RAJU	SVP COLLEGE OF ENGINEERING	560
9	VOOTA VENKATA RAMANA	JKR ENGINEERING	500
-0	KOLLEFARA SANTHOSH	VITAN	500
11	GANTHA SANTHI KUMAR	V/IAM	300
12	B SANYASI NAIDU	AUCE VISARHAFATNAM	900
12	R V RAJENDRA PRASAC	APSHC ATRIAN	590
14	R SRINIVASA RAO	SETET	500
15	CH SUCHER	WIT	500
16	K GANESH KEWSR	OSEDCING	aCO
17	MHARKA	PYGAN ENG SOLUTES	593
18	2 SSI DEVI	VIT.	500
16	PKAGGA	let -	ECC
20	SHAVESH	COASTAL NOT	533
21	W SARATH CHENDRA	COASTAL INST	500
22	M FAVAN KUMAR	SAP ENG COLLEGE	500
13	S SALDWAKAR	VCIPL	500
24	K V MIAYA KUMARA RAJU		500
22	MAKARI A SUSATHA KUMARI	MVGR COLLEGE OF FNGINLEAING PG SCHOLAR MVGR COLLEGE OF FVGINLERING PG SCHOLAR	500
6	WANKANTHA LENDI	MYGH CELLEGE OF ENGINEERING PG SCHOLAR	500
)7	PAGATHI SANKARA MOULI	MYGR DOLLEGE OF ENGINEERING, PG SCHOLAR	600
26	TAMISEN SRAGAN KUMAR	MyGRECOLLEGE OF ENGINEERING FO SCHOLAR	500
9	BACTULA ROHIKA	WYOR COLLEGE OF EVOING FOR SCHOLAR	500
n	DUSI SRUJANA SR	MYGRICELLEGE OF ENGINEERING, PG SCHOLAR	300
1	JALLU DHIMAMALU	MAGRICULTUS OF ENGINEERING, PG SCHOLAR	000
2	K JAGADEESWARI	MARK COLLEGE OF ENGINEERING PG SCHOLAR	500
3	RAPAKIH DANESH	AVGN COLLEGE OF EXCHIPERING PG SCHOLAR	500
	S V SA KUWAR	MYGR COLLEGE OF ENGINEERING, PG SCHOLAR	000
5	VIVSANSHA	MWGR COLLEGE OF ENGINEERING, PG SCHOLAR	603
9	V WADSIKTIS IMA	MVSH COLLEGE OF FVGINEEHING PG SCHOLAR	500
	E PRASANA BABU	MYSR COLLEGE OF FINGINGERING PG SCHOLAR	500
1	G FRATYUSHA	MOVER COLLEGE OF FINGING PRINCIPE SCHOLAR	500
	K SUNIL KUMAR	MVGR COLLEGE OF ENGINEERING PG SCHOLAR MVGR COLLEGE OF ENGINEERING PG SCHOLAR	803
	K SA KIMAR	MYGR COLLEGE OF ENGINEERING PG SCHOLAR	500
	K PRASONTH KUROR	MVGR COLLEGE OF ENGINEERING PG SCHOLAR	500
	K KALYANA CHAKRAVARTHY	MVGR COLLEGE OF ENGINEERING, PG SCHOLAR	626
	PIVONICA	MYCH COLLEGE OF EVAINTERING PO SCHOLAR	630
1	SARUS KUMAR	MYDR COLLEGE OF ENGINEERING, PG SCHOLAR	100
	OHK SAMEER	MVGR COLLEGE OF ENGINEERING PG SCHOLAR	30,6
	SICERA	MYGR GOL FOL OF ENGINEERING PG SCHOLAR	600
Y	COPALA KRISHNA	WYSR COLLEGE OF ENGINEERING PG SCHOLAR	500
	ANEGLA	MAGE COLLEGE OF EXCHAFTERING, PC SCHOLAR	500
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	VERMATA SURYA KUMARI	MAC COUNTER OF PROPERTY	
P	VERMATA SURMA KUMARI KALGRINIMAS	MACE COLLEGE OF ENGINEERING PO SCHOLAR WAGE COLLEGE OF ENGINEERING PO SCHOLAR	500 500

Organization Secretary

HOD

		Registration form for M. Tech St.	
SI.	REGD, NO.	STUDENT NAME	Signature
No.	1433108701	BANKA VENKATA KIUSHNA	Absent
14	1433138702	C SRINEVAS SWAYAMVARAPU	ACINE
2	1433108702	PASTIL HARS BILLA	- Armet
3	14331D3703	ROLLIPARA V G MANI KANTA SREE BAM	About
5	14331D8705	KUTCHARLAPAT V JAVA KUMARRAJU	To Remain on
6	14331D8706	MAKASTA SUSMICHA KUMARI	Allender
7	14331D8707	MANIKANCA LA DI	Million
8	1433108708	MATA PRAGNA	- CARSON -
g	14331D8709	PRATHA PAR MALA	Abrut
10	1433108710	RAGATHI SANKADAMOULI	122- Mouli
t1	1433108711	SALADI SUSANNA	- Assure -
-2	1433108712	TAMMINENI SRAVAN KUMAR	T. 24.
:3	14331D8713	TUV BALL DIVYA MANASA	A Days
4	14931.08714	URITI PRADESP	- Alexander
-5	1533108700	BATTH BAMESE KUMAR	- CABINAD
16	1593108702	BATTULA ROHIKA	Pohita_
17	15331DH703	BOODSDAIV S VAMSINADII	- (Abbert) -
18	2.5231DHZ0/5	DHARMANA CHINNA BABO	+ Best 5
19	15331108705	DUSTSRUJANASRI	D. Danscon Soil
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## **5. PHOTOGRAPHS**





## A Report on Six Day Faculty Development Program

On

Real-Time Applications Using LabVIEW (RTAL-2016) 28<sup>th</sup>November - 3<sup>rd</sup> December 2016 Organized by

# Department of Electrical and Electronics Engineering MVGR College of Engineering (Autonomous)

(Listed Under 2(f), 12(b) Act of UGC)

(Permanently Affiliated to JNTU, Kakinada, and Approved by AICTE & NBA, New Delhi, Accredited with "A" grade by NAAC)
VIZIANAGARAM

### **Introduction:**

As the engineering education and computer technologies for industry merge, the opportunities for teaching and learning will also expand. In such context, a large number of academic courses that teach electrical engineering concepts have incorporated the graphical programming as educational tool for student use. The virtual instrumentation approach pioneered by LabVIEW may enhance the electrical engineering curricula.

As the graphical programming has grown in popularity, its usage in the educational field has become an important preoccupation for more and more universities all over the world. In electrical engineering, graphical programming environments like NI LabVIEW have now the potential to enhance the content of a large variety of courses , offering teachers, the opportunity to develop new educational tools in the form of "virtual instruments".

LabVIEW (Laboratory Virtual Instrumentation Engineering Workbench) is an industrystandard, fullyfeatured graphical programming language, offered by National Instruments since 1986 and extensively used for developing test, control and measurement applications. Because LabVIEW dramatically simplifies and accelerates the design projects, the educators have incorporated LabVIEW in a large variety of courses, from introductory classes to graduatelevel studies. It can be used both in classrooms (or laboratories), for inclass concept demonstrations and prototype implementation, and in homework, to compute, simulate and identify solutions to engineering problems. Note that, because the appearance and operation of the LabVIEW programs imitate physical instruments, such as oscilloscopes or multimeters, they are called virtual instruments or VIs.

## Features:

The LabVIEW programming is extremely intuitive. Disposing of various objects, such as terminals, constants, structures, functions and subVIs (subvirtual instruments), LabVIEW allows users to define the behavior of their applications by dragging and dropping these objects onto a diagram, and connecting them through wires, as necessary. Each terminal in the block diagram has a correspondent on the front panel window (the graphical user interface of the virtual instrument), which acts either as a data input or a data output. Generally, this programming approach is very well received by students, many of them stating that the LabVIEW platform is highly accessible and efficient for learning.

LabVIEW comes with hundreds of readytouse example VIs. Moreover, it integrates configurable Express VIs that significantly reduce the development time and complexity associated with adding analysis and signal processing algorithms into user applications. Finally, instructors and students may save time and acquire new ideas by visiting the National Instruments' academic web site, where they can access labs, exercises and tutorials covering a large number of electrical engineering disciplines.

## The Objectives:

- 1.Introductionto LabVIIEW
- 2.Data Acquisition and control application using NI Tools
- 3. Application development using LabVIEW with realtime examples
- 4. Hands On practice with NIDAQ
- 5.Usage of relevant toolboxes in LabVIEW

## **Applications:**

- Control Design and Simulation: combine algorithm development, analysis and visualization in LabVIEW with tools for system identification, control design, simulation and implementation;
- Industrial Monitoring and Control: deploy LabVIEW to networked programmable automation controllers (PACs) for creating reliable distributed monitoring and control systems, and connect to existing programmable logic controllers (PLCs) and enterprise systems;
- Image and Signal Processing: incorporate hundreds of image and signal processing functions into LabVIEW applications;
- Software Development and Deployment: develop and deploy professional, higherquality LabVIEW applications using a variety of software engineering tools;
- Report Generation and Data Storage: export acquired data to thirdparty software tools, such as Microsoft Office and industrystandard databases, and publish information to the Web.

### **Event Details:**

Title: Realtime applications using LabVIEW

Duration: No of participants: 63

Budget: Rs 2, 00,000/-

## Inaugural:

**Monday**(28-11-2016): A Faculty Development Program (FDP) on Lab VIEW applications in Electrical Engineering was organized successfully during  $28^{th}$  Nov  $-3^{rd}$  Dec 2016 by the EEE department of MVGR College of Engineering, Vizianagaram under the able mentorship of . Dr. K. V. L. Raju, Principal and Prof. D. J. J. Ramachandra Raju, Vice-Principal (Administration) of MVGR College of Engineering .The resource persons have been very keen to offer hands-on skills to faculty members in the domain of cutting-edge technologies to train students effectively to bridge gap between academia and industry.

Daywise report:

Monday(28-11-2016): The inaugural was followed by an introductory session on LabVIEW titled session "
Engineering solutions using Graphical Systems Design WHY? WHAT? HOW? by Mr Mani Maran,(NI)

National Instruments

The remaining sessions were handled ably by Mr Suresh Venkatesan ,VI solutins Pvt ltd , on structures,loops and concepts in LabVIEW

**Tuesday(29-11-2016):** The arrays, custers , graphs and charts and FileI/O concepts in LabVIEWwere explained in this day session by Mr Suresh Venkatesan ,VI solutins Pvt ltd

**Wednesday**(30-11-2016) This enthralling session was on Introduction to Signal Processors" by Dr. D.K. Mohanta, a renowned teacher ,which were well received by the audience. The afternoon session was taken up on "Introduction to Multisim" by V. Jagadeeshan and Rajasekhar

**Thursday** (31-11-2016): The continuationtopic was on "Multisim - LabVIEW Co-simulation for the Power Electronics Applications" by V. Jagadeeshan and Rajasekhar.ably explained by both.

**Friday (01-12-2016)** ;The realtime applications of LabVIEW were in full view in the session Automation of Electrical machines using CRIO platform by V. Jagadeeshan and Rajasekhar.followed by a sessionon "myRIO based PWM generation for the DCDCconverter (BUCK/BOOST) by V. Jagadeeshan and Rajasekhar.

**Saturday (02-12-2016)**The realtime applications were followed up in the topic "Power System & Power Quality Analysis using the Electrical Power Suite "by V. Jagadeeshan and Rajasekhar

**Saturday**(03-12-2016): The last day was on the topic Electrical Engineering Applications using LabVIEW by Dr. M.J.B.Reddy.which was really great and well received by the audience. followed by valedictory session.

The workshop was well attended .There were 63 participants from various colleges with in house faculty 24 members and 6 studetn members. The workshop was grand success as is proved by the enthusiastic participants and their feedback.

A FACULTY DEVELOPMENT PROGRAM on 'REAL-TIME APPLICATIONS USING LABVIEW' (RTAL-2K16)

28th November - 3rd December, 2016

## REGISTRATION FORM

Participant's Name :
Designation :
Department :
Organization :
Address :
Phone no :
Email ID :

Registration details

DDNo.: Date: Amount in Rs.: Bank:

Signature of the participant

## Sponsorship certificate

Certified that Mr./Ms./Mrs. \_\_\_\_\_\_ is a student/faculty/employee of our institution /industry and is hereby sponsored to attend Faculty Development Program on "Real-time applications using LabVIEW"at MVGR College of Engineering, Vizianagaram from 28<sup>th</sup> November to 3<sup>rd</sup> December 2016.

Place:

Date : Signature of sponsoring Authority with seal Patron

Sri P. Ashok Gajapathi Raju Chairman, MANSAS

President

Dr. K.V.L. Raju

Principal, MVGR College of Engineering

Advisory Board

Dr. D. R. K. Raju

Correspondent, MANSAS

Prof. D.J.J.R. Raju

Vice-Principal (Administration)

Dr. Y.M.C. Sekhar

Vice-Principal (Academics)

Dr. D. R. PrasadaRaju

Dean (Faculty Development)

Dr. P. Ravindranath

Dean (Strategic Planning)

Dr. M. Sunil Prakash

Dean (Training & Placements)

Dr. K. Rajeswara Rao

Dean (Civil Infrastructure)

Dr. R. Ramesh

Dean (Research & Development)

Dr. S. Srinivasa Rao

Dean (Student Affairs)

Dr. T.V.N. Partha Sarathi

Dean (Examinations)

## Organizing Committee

Mr. K. PrasadaRao, Assistant Professor

Mr. K.V.V.Prasad, Assistant Professor

Mr. P.A.MohanaRao, Assistant Professor

Mr. Ch.BhavaniSankar, Assistant Professor

Mr. P.Pavan Kumar, Assistant Professor

Mr. Ch.Satyanarayana, Assistant Professor Mr. B.Jagannadh Ch. Yadav, Assistant Professor

Mr. T.Sudhakar, Assistant Professor

Mr. K.Sri Ram, Assistant Professor

Mrs. K.KrishnaKumari, Assistant Professor

Mr. P.Gurumurthy Reddy, Assistant Professor

Mr. S.M.K.Patnaik, Assistant Professor

Mr. G. Shankarmani, Assistant Professor

Mr. D.Ravi Kumar, Assistant Professor

Mr. D.Rajesh, Assistant Professor



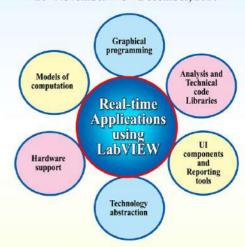


## A FACULTY DEVELOPMENT PROGRAM

on

'REAL-TIME APPLICATIONS USING LABVIEW'
(RTAL-2K16)

28th November - 3rd December, 2016



Dept. of Electrical & Electronics Engineering
MVGR College of Engineering (Autonomous)
(Approved by AICTE, New Delhi, Accredited by NBA
of AICTE, Graded 'A' by NAAC of UGC and Permanently
Affiliated to JNTUK)

Chinthalavalasa, Vizianagaram - 535005 Andhra Pradesh

Visit us at: www.mvgrce.edu.in
Mail us to: rtal2k16.mvgr@gmail.com

## **About College**

Maharaj Vijayaram Gajapathi Raj College of Engineering was established in the year 1997 under the aegis of "MANSAS" educational trust, founded by the Raja Saheb of Vizianagaram, Dr. PVG Raju in 1958. The college is located at Vijayaram Nagar campus, Chintalavalasa, Vizianagaram, Andhra Pradesh. The college offers 7 B.Tech. programmes namely EEE, ECE, Mechanical, Civil, CSE, Chemical Engineering and IT & P.G. programmes in Power Systems, Machine Design, VLSI, CSE, Structural Engineering, CNIS and Business Administration. It is accredited with "A" grade by NAAC of UGC. Also UGC has conferred Autonomous status to the college. The college gifted with serene atmosphere amidst green plantation is making its untiring efforts to attain its objective of imparting qualitative technical education. In addition to curricular programs, the institute constantly strives to update its faculty and students by conducting Workshops, Conferences, Seminars and Guest lectures in the emerging fields.

## **About Department**

The state-of-art Department has infrastructure that caters to the outcome based teaching-learning. It has a strong team of qualified & experienced faculty members with a right blend of industry, institution and R&D exposure that supports teaching-learning requirements. In addition to curricular exposure, department strives to keep students aware of the pragmatic needs of the industry for the improvement of employability and professional development. Keeping this in view department has signed MOU with SIEMENS India Pvt. Ltd. to bridge the gap between industry and academia for imparting training to students in the areas of AC/DC Drives, PLC-Automation etc. and it has been equipped with MATLAB, LabVIEW, PSCAD etc.

#### Theme of the program

LabVIEW (Laboratory Virtual Instrument Engineering Workbench) from NI (National Instruments), a development environment designed specifically to accelerate the productivity of engineers and scientists, is commonly used for data acquisition, instrument control, and industrial automation on a variety of platforms. With a graphical programming syntax that makes it simple to visualize, create, and code engineering systems, LabVIEW is unmatched in helping engineers translate their ideas into reality, reduce test times, and deliver business insights based on collected data. From building smart machines to ensuring the quality of connected devices, LabVIEW has been the preferred solution to create, deploy, and test the Internet of Things for decades. This workshop aims to provide a forum for exposition of state-of-the-art for different applications in electrical discipline.

## Objective of the program

This program acquaints the participants with LabVIEW in electrical engineering applications. It is aimed to bring together reputed national level experts to exchange knowledge in LabVIEW for diverse electrical applications. The workshop will be beneficial for academicians, practicing engineers and researchers.

This course provides an insight into:

- · Introduction to LabVIEW Programming
- Usage of NI tools in data acquisition and control applications
- Application development using LabVIEW with real time examples
- Hands-on practice with National Instrument (NI) DAO
- Usage of relevant toolboxes in LabVIEW

#### Resource Persons

- 1. Dr. D.K. Mohanta, BITS, MESRA
- 2. A Manimaran, Senior Applications Engineer, VI Solutions, Bangalore.
- Suresh Venkatesan, Senior Applications Engineer, VI Solutions, Bangalore.
- Mr. Rajasekar, Sr. Application Engineer, Innovative Invaders Technologies
- Mr.Visweswaran Jagadeesan, Sr. Application Engineer

## Convener

Dr. Sarat Kumar Sahu, Professor and Head

#### Co-Conveners

Dr.R. Gowrisankara Rao, Professor Mr.K.S. Ravi Kumar, Associate Professor Mr.I. Kranthi Kiran, Associate Professor Mr.M. Venu Madhav, Associate Professor

#### Coordinator

Mr. P Sai Srinivas Senior Assistant Professor Mobile: +91 9440274834 Phone: +91 8922 241167(O) E-mail: patnanasai@gmail.com

## College website: www.mvgrce.edu.in

Department of EEE, MVGR College of Engg.

## Registration fee

Faculty participant	Rs. 1000/-
Industryparticipant	Rs. 2000/-
Studentparticipant	Rs. 500/-

Registration fee is payable through Demand draft drawn in favour of Principal, MVGR College of Engineering, Vizianagaram, on or before 22<sup>nd</sup> November, 2016. The registration form can be downloaded from the college web site.

The filled-in formcan be sent to Mail-ID: rtal2k16.mvgr@gmail.com

## A Six-Day Faculty Development Program

On

## Real-Time Applications Using LabVIEW (RTAL-2016) 28th Nov – 3rd Dec 2016

Organized by

# Department of Electrical and Electronics Engineering MVGR College of Engineering (Autonomous)

(Listed Under 2(f), 12(b) Act of UGC)
(Permanently Affiliated to JNTU, Kakinada, and Approved by AICTE & NBA, New Delhi, Accredited with "A" grade by NAAC)

nently Affiliated to JNTU, Kakinada, and Approved by AICTE & NBA, New Delhi, Accredited with VIZIANAGARAM

## **Program Schedule**

Day	Time	Event
-	9:30- 10:45	Inaugural Function
	10:45-11:00	Tea Break
	11:00-01:00	Engineering solutions using Graphical Systems Design WHY? WHAT? HOW?  By Manimaran
Monday	01:00-02:00	Lunch Break
28-11-2016	02:00-03:00	Graphical Systems Design Using NI's LabVIEW & Hands on LabVIEW
		Programming concepts – Structures by Mani Maran and Suresh Venkatesan
	03:00-03:15	Tea Break
	03:15-04:15	Hands on LabVIEW Programming concepts – Loops by Mani Maran and Suresh Venkatesan
	9:30- 11:00	Hands on LabVIEW Programming concepts - Arrays by Mani Maran and Suresh Venkatesan
	11:00-11:15	Tea Break
Tuesday	11:15-01:00	Hands on LabVIEW Programming concepts – Clusters by Mani Maran and Suresh Venkatesan
Tuesday	01:00-02:00	Lunch Break
29-11-2016	02:00-03:00	Hands on LabVIEW Programming concepts - Graphs & Charts by Mani Maran and Suresh Venkatesan
	03:00-03:15	Tea Break
 	03:15-04:15	Hands on LabVIEW Programming concepts - FILE IO by Mani Maran and Suresh Venkatesan
	9:30- 11:00	"Introduction to Signal Processors" by Dr. D.K. Mohanta
	11:00-11:15	Tea Break
Wednesday	11:15-01:00	Overview on Importance of LabVIEW in Electrical Engineering Applications by Dr. D.K. Mohanta
30-11-2016	01:00-02:00	Lunch Break
00 11 2010	02:00-03:00	Practice Session
	03:00-03:15	Tea Break
	03:15-04:15	Practice Session
	9:30- 11:00	"Introduction to Multisim" by V. Jagadeeshan and Rajasekhar
	11:00-11:15	Tea Break
	11:15-01:00	"Multisim - LabVIEW Co-simulation for the Power Electronics Applications" by V. Jagadeeshan and Rajasekhar
Thursday	01:00-02:00	Lunch Break
01-12-2016	02:00-03:00	Automation of Electrical machines using cRIO platform by V. Jagadeeshan and Rajasekhar
	03:00-03:15	Tea Break
	03:15-04:15	Automation of Electrical machines using cRIO platform by V. Jagadeeshan and Rajasekhar
	9:30- 11:00	"myRIO based PWM generation for the inverter" by V. Jagadeeshan and Rajasekhar
	11:00-11:15	Tea Break
	11:15-01:00	myRIO based PWM generation for the DC-DC converter (BUCK/BOOST) by V. Jagadeeshan and Rajasekhar
Friday	01:00-02:00	Lunch Break
02-12-2016	02:00-03:00	Power System & Power Quality Analysis using the Electrical Power Suite by V. Jagadeeshan and Rajasekhar
	03:00-03:15	Tea Break
	03:15-04:15	Power System & Power Quality Analysis using the Electrical Power Suite by V. Jagadeeshan and Rajasekhar
	9:30- 11:00	Electrical Engineering Applications using LabVIEW by Dr. M.J.B.Reddy
Saturday	11:00-11:15	Tea Break
03-12-2016	11:15-01:00	Electrical Engineering Applications using LabVIEW by Dr. M.J.B.Reddy
03-14-2010	01:00-02:00	Lunch
	02:00-03:00	Valedictory Function









Two Day National Workshop on Emerging Trends in Automotive Technologies  $August \ 1^{st} \ \& \ 2^{nd} \ 2016$ 

Organized by: DEPARTMENT OF MECHANICAL ENGINEERING MVGR COLLEGE OF ENGINEERING (AUTONOMOUS)

#### **Objective of the Workshop:**

Although internal combustion engines have been used for more than a century, significant improvements in engine combustion, thermal efficiency and emissions reduction are still possible. The objective of the workshop on "Emerging trends in Automotive Technologies" will discuss an overview of current trends in automotive fuel, combustion and emission reduction technologies. The program will also cover the basics and performance testing of the engine. This two-day workshop provides the platform for faculty to interact with experts from academic and industry in the area of automotive technology. It will also help the faculty to enhance their knowledge and to do research with modern tools and technologies.

#### **Resource persons:**

- 1. DrSimhachalamJuttu, Deputy General Manager, Powertrain Engineering, ARAI, Pune
- 2. Dr S. S. Thipse, Deputy Director, Powertrain Engineering, ARAI, Pune
- 3. MrYogeshKalia, Director of Medhaavi Center for Automotive Research, Punjab

#### **Topics covered:**

Engine basics, Performance Testing and combustion analysis

Pollution Reduction Techniques - EGR/Turbocharging/CRDI

Advanced Fuel Efficiency and Combustion Technologies- HCCI, PCCI, GDI, OPOC

Alternate Fuel Technologies - Future Emission Regulations and Road Map for India

Engine Management, Controls, Measurement and Calibration

#### ORGANIZING COMMITTEE

Sri P Ashok Gajapathi Raju, Chairman, MANSAS (Union Minister for Civil Aviation)

#### PATRON

Dr K V L Raju, Principal

#### ADVISORY COMMITTEE

Dr D R K Raju Dr D J J Ramachandra Raju Dr Y M C Sekhar Dr D R Prasada Raju

Dr R Ramesh Dr P Ravindranath

Correspondent Vice Principal (Admin) Vice Principal (Academic) Dean (F&D) Dean (R&D)
Dean (Strategic Planning)

Sri P Ranga Raju Asst. Principal (Admin)

#### CONVENER

Professor & HOD Dr S Adinarayana

#### COORDINATOR

Dr N Ravi Kumar Professor

#### MEMBERS

#### Associate Professors

Dr L V V Gopala Rao Dr I Sudhakar Sri Praveen Kalla Sri B A Ranganath Dr S Srinivasa Rao Sri M Anil Prakash

#### **Assistant Professors**

Sri Umamaheswara Raju R S Sri K Ajay Sri P Sreenu Sri K Ajay Sri S Sanyasi Naidu Sri Varun Chebrolu Sri G Pramod Kumar Sri S Joshua Kumar

Sri N Jagadeesh Sri N Abhishek

Sri K Ravi Kumar Sri D Santhosh Kumar

Sri B MadhavaVarma Sri G Satynarayana

Sri G Rajesh Mrs S Jyothirmai Sri M Y Prasada Rao Sri G Veeraiah

Sri K Pavan Kumar Sri B Srinivas Sri N Murali Krishna

TOPICS TO BE COVERED

- Advanced Fuel Efficiency and Combustion Technologies: HCCI, PCCI, GDI, OPOC.
- Alternate Fuel Technologies Future Emission Regulations and Road Map for India. Engine Management, Controls, Measurement and

Faculty and Industrial Participants: Rs. 1000/-Ph.D./M. Tech Students from Engineering Colleges and Universities: Rs. 500/-Fee includes Communications

#### MAXIMUM NUMBER OF PARTICIPANTS

LAST DATE FOR RECEIPT OF APPLICATION

FOR CORRESPONDENCE
Dr. S. Adinarayana
Professor & HOD
Email: mechhod@mvgrce.edu.in
Phone: +91 - 9440584131, 08922-241038





#### Two-Day National Workshop on **Emerging Trends in Automotive Technologies**



1st & 2nd August 2016



#### Organized by

Department of Mechanical Engineering MVGR COLLEGE OF ENGINEERING (Autonomous) (Approved by AICTE, New Delhi, Re Accredited by NBA, Re Graded 'A' by NAAC,

and Permanently Affiliated to JNTU - Kakinada, Listed U/S 2(f) & 12(8) of the UGC Act 1956) Vizianagaram - 535 005 Andhra Pradesh

Maharaj Alak Narayan Society of Arts and Sciences (MANSAS) an educational trust with an altruistic approach, was established in 1958 in the name and in the hallowed memory of the father of Dr. P V G Raju. By establishing the trust, Dr. P V G Raju brought of Dr. P V G Raju. By establishing the trust, Dr. P V G Raju brought ducation from preparatory to post graduation under a single umbrella sacrificing even his royal palace which is now glorified as the 'Citadel of Learning'. The trust, under the guidance of Sr. P. Ashok Gajapathi Raju, Chairman, MANSAS and Union Minister for Civil Aviation is developing with great potential by offering graduate and post graduate programmes in diversified disciplines such as engineering, management, pharmacy etc. The mission of the trust is purely 'service to society' by providing quality education and striving for social uphringing. quality education and striving for social upbringing.

#### INSTITUTIONS UNDER MANSAS

There are 12 Institutions under the trust offering education from KG to PG level education in Arts, Science, Engineering, Medicine and Management:

- M. R. High School
   M. R. Girls High School
   M. R. College of Education
   M. R. Model High School
   M. R. Women's college
   M. Women's college
   M. Wid R. College of Engineering (A)
   Maharajah's Post Graduate College

#### ABOUT THE INSTITUTION

Maharaj Vijayaram Gajapathi Raj College of Engineering is an autonomous institution established in the year 1997 under the aegis of the MANSAS Educational Trust. The college offers 7 U.G. and 7 P.G. Programs:

- All eligible UG Programs (CHEMICAL, CIVIL CSE, ECE, EEE, IT & MECHANICAL) were re-accredited by NBA.
   CSE, ECE, CHEMICAL and MECHANICAL departments are

- CSE, CLE, CHERUICAL and MECHANICAL departments are recognized as RESEARCH CENTERS by JNTU-K.
   The institution has 249 faculty with 55 Ph.D.Degree Holders.
   Above 50,000 volumes and Access to 8 international online journal packages like IEEE, SPRINGER, JGATE, ELSEVER.
   1089 Systems & 200 MBPS Band width internet facility.
   The college campus spread in 60 acres with Total built up area of about 5 Lakh sft.
   The institution established 83 Laboratories with an
- The institution established 83 Laboratories with an
- investment of about 10 crores.

  Research projects worth of Rs 2 crores are ongoing.

  WIPRO MISSION 10X technology learning center in the

- Video College.
   TCS Accredited, ALP & AIP collaborating institution.
   Identified as Business Incubator, by Ministry of MSME, New
- Recognized as SDC by Government of Andhra Pradesh MoU with Andhra Cricket Association for building sports.

#### ABOUT THE DEPARTMENT

The Department of Mechanical Engineering has been in existence since the inception of the college i.e. 1997. The Department offers Undergraduate program in Mechanical Engineering with an intake of 180 and a Post Graduate program in Machine Design with an intake of 18.

- 40 well qualified faculties with 10 Ph.D. holders
  14 laboratories including research lab with state of art
- equipment.

   Recognized as Research Center by JNTU-K.

   Accomplished Research projects worth more than 65 Lakhs sponsored by DST, UGC and AICTE.

   PTC recognized 'Center of Excellence'.
- Tripartite agreements with AP state government and SIEMENS for skill development. MOUs with Adriotec, Kriatec Services, Zues Numerix, PTC, Askar Microns, Dr.Reddy's and Talent Sprint.

- Runs add on programs in Design and Manufacturing, Recognized Skill Development Center for Welding & Automotive Training by APSDC. Gold medals for academic excellence from JNTUK in 2001, 2013 & 2015.
- Active participation in competitions organized by professional bodies such as SAE, IE, ISTE, ISIE, ISNEE etc.

#### OBJECTIVE OF THE WORKSHOP

OBJECTIVE OF THE WORKSHOP
Although internal combustion engines have been used for more than a century, significant improvements in engine combustion, thermal efficiency and emissions reduction are still possible. The objective of the workshop on "Emerging trends in Automotive Technologies" will discuss an overview of current trends in automotive fuel, combustion and emission reduction technologies. The program will also cover the basics and performance testing of the engine. This two day workshop provides the platform for faculty to interact with experts from academic and industry in the area of automotive technology. If academic and industry in the area of automotive technology. It will also help the faculty to enhance their knowledge and to do research with modern tools and technologies.

#### RESOURCE PERSONS

- Dr. S S Thipse, Deputy Director, Powertrain Engineering, ARAI, Pune. Dr. Simhachalam Juttu, Deputy General Manager,
- Powertrain Engineering, ARAI, Pune. Mr. Yogesh Kalia, Director of Medhaavi Center for

#### Two Day National Workshop on **Emerging Trends in Automotive Technologies**

#### REGISTRATION FORM

1. NAME OF THE APPLICANT (IN BLOCK LETTERS):
Mr./Ms.:
2. DESIGNATION:
3. ORGANIZATION:
4. MAILING ADDRESS WITH PIN CODE:
TELEPHONE:
MOBILE:
EMAIL:
5. DETAILS OF REGISTRATION FEE:
Date: Signature of Applicant

NOTE: Please send the registration form along with DD (in favour of Principal, MVGR college of Engineering, Payable at Vizianagaram) on or before 20th July, 2016.
Scanned copy may be sent to the following e-mail: mechhod@mvgrce.edu.in



## DEPARTMENT OF MECHANICAL ENGINEERING MVGR COLLEGE OF ENGINEERING (A): VIZIANAGARAM

Two Day National Workshop on Emerging Trends in Automotive Technologies August  $\mathbf{1}^{st}$  and  $\mathbf{n}^{d}$  2016

Date	Time	Lecture No.	Guest / Speaker	Topic Name
	9:00 - 9:30		Reporting	and Registration
	9:30 - 10: 00		Worksho	p Inauguration
(6)	10:00 -11:30	01	Dr S. S. Thipse	Alternate Fuel Technologies - Future Emission Regulations and Road Map for India
2016	11:30 -11:45		Tea Br	eak
80	11:45 -13:15	02	Dr S. S. Thipse	On Board Diagnostics (OBD)
01-	13:15 -14:00		Lunch B	reak
Day 1 (01-08-2016)	14:00 - 15:30	03	Dr Simhachalam Juttu	Engine basics, Performance Testing combustion analysis and Bharat Stage Regulations (Emissions and OBD)
	15:30 -15:45		Tea Br	eak
	15:45 - 17:00	04	Dr Simhachalam Juttu	Pollution Reduction Techniques - EGR/Turbocharging/CRDI/ After-treatment system, etc.
	9:30 -11:30	05	Mr Yogesh Kalia	Engine Management, Controls, Measurement and Calibration
	11:30 -11:45		Tea Br	eak
Day 2 (02-08-2016)	11:45 - 13:15	06	Dr Simhachalam Juttu	Advanced Fuel Efficiency and Combustion Technologies – Fuel efficiency technologies, Combustion photography, HCCI, PCCI, GDI, OPOC, water injection, etc.
05-(	13:15 -14:00		Lunch B	reak
Day 2 (	14:00 -15:30	07	Dr Simhachalam Juttu	Advanced Fuel Efficiency and Combustion Technologies – Fuel efficiency technologies, Combustion photography, HCCI, PCCI, GDI, OPOC, water injection, etc.
	15:30 -15:45		Tea Br	eak
	15:45 -16:45		Valedictory Function	and Certificate Distribution

#### MITOR COLLEGE OF ENGINEERING (ACTOMOROUS)

#### Department faculty participants list:

S.No	Name	Designation
1.	Dr S Adinarayana	Professor
2.	Dr N Ravi Kumar	Professor
3.	Sri P Ranga Raju	Associate Professor
4.	Dr L V Venu Gopala Rao	Associate Professor
5.	Dr I Sudhakar	Associate Professor
6.	Sri K Praveen	Associate Professor
7.	Sri M K Naidu	Associate Professor
8.	Sri B A Ranganath	Associate Professor
9.	Dr S Srinivasa Rao	Associate Professor
10.	Sri M Anil Prakash	Associate Professor
11.	Sri R S Umamaheswara Raju	Asst.Professor
12.	Sri K Ajay	Asst.Professor
13.	Sri S Sanyasi Naidu	Asst.Professor
14.	Sri Ch Varun	Asst.Professor
15.	Sri B Madhava Varma	Asst.Professor
16.	Sri G Satyanarayana	Asst.Professor
17.	Sri G Rajesh	Asst.Professor
18.	Sri M Y Prasada Rao	Asst.Professor
19.	Sri G Veeraiah	Asst.Professor
20.	Sri K Pavan Kumar	Asst.Professor
21.	Sri N Murali Krishna	Asst.Professor

## DEPARTMENT OF MECHANICAL ENGINEERING MVGR COLLEGE OF ENGINEERING (A): VIZIANAGARAM

Two Day National Workshop on Emerging Trends in Automotive Technologies

	ast .	and anse
August	1 and	2 <sup>nd</sup> 2016

S.No	Name of the Participant	Mail ID
1	J. Surendra	iasti so rendra @ quail rom
2	D. Ravi	RAVID: GMAIL P. gmark tem. RAVID: 346 @ gmail: Com.
3	M. Vined Babu	mv1036@9mail.com (9490248582)
4	K. Sin Rama Hoer thy	VSDn05 & gmail Com (9951373443)
5	N. Bulli Roju	bulli vaju 7411 @ gonailoan (898543600)
6	M. PREM DHEERAT	mpre moheeraj, me a anits.edu.in.
7	J. V BHANUTEJ	bhanutej.me@anits.edu.in
8	Oma maharusara pan 25	maleshagie @ nugsio eder in.
9	C. Vernagh	Veenument 3070 amol com
10	S. Jochara Kimas	inshuokuma @ myorca. ed. in
11	G. Saljanayana	Satyunareyanagogula Omora eduis 9 95,506684
12	M. Y. Prasada Row	myprasaderovgree- edu. in (9492929166)
13	Mil yousuf ALI	you suf @ my gree. edu, in
14	Nalluri AbinShek	Mallow ashi hele annal - Con
15	KAJan	along-konapala (2) migra edurin
16	K.T. Jagescondidan naid	Jogish Kf 1 @ gmail · Com.
17	9. pramod kuman	Pramokumar & mygrae-edu in
18	or 4. Huli krisha	murchi krishna. Yamala e gmail. ram
19	K. Mohan leumor	mohumber 79 @ gmail. Com
20	G. SURYA	·Surya. chardra 12221@gmail.com
21	B. Madhava Varme	madhavvarmagygrce.edu.in
22	N. Munal Kodelina	Kojshna @ mugr ce ale. in
23	K. Roui Kumay	Kattala sawikuman ir @ 9 mail.com
24	K. Pavan Kuman	Parankmech DImail.com

## 2017-18

#### REPORT ON NATIONAL WORKSHOP ON HYDROLOGIC AND HYDRAULIC MODELING USING MIKE SHE & MIKE HYDRO RIVER CONDUCTED FROM 31-10-2018 TO 2-11-2018

#### ORGANIZING COMMITTEE

#### CHIEF PATRON

Sri. P Ashok Gajapathi Raju, Chairman, MANSAS Ms. P Aditi Gajapathi Raju, Member, Trust Board

PATRON

Dr. KVL Raju, Principal, MVGR

ADVISORY BODY
Dr. DRK Raju, Correspondent, MANSAS

Dr. YMC Sekhar, Vice-Principal

Dr. Ch Purnachandra Rao, Dean (Accreditations & Estt)

Dr P.Ranga Raju Dean(Administration)

Dr P.S.Sitarama Raju Dean(Quality Assurance)

Dr. R Ramesh, Dean (Research & Development)

Dr. K Rajeswara Rao, Dean (Civil Infrastructure)

Dr. DR Prasada Raju, Dean (Faculty Development) CONVENOR

Dr. P Markandeya Raju, Professor & HoD

### ORGANIZING SECRETARIES Dr. S Chandramouli, Professor

Dr. R Maheswaran, Associate Professor COORDINATORS

Mr. A Varaprasad, Asst. Professor Mr. Kalyan AVS, Asst. Professor FACULTY MEMBERS

Dr. Partheepan Ganesan

Mr. S Murali Sagar Varma Mr. Ch V Ravi Sankar

Mr. V Vinay

Dr. P Sudheen

Mr. BV Joga Rao

Mr. S Purushotham Rao

Mr. K Santosh Kumar

Ms. T Jahnavi

Mr. A Sai Kumar

Mr. SSB Sai Kumar Mr. W Sai Deepak Mrs. D Praseeda Mr. G Rahul Reddy

Sri. B Ramesh Raju

Mr. RP Singh

Mr. B Ramu

Mr. BVSSR Bhaskar

#### Who can attend?

Faculty Members, Research Scholars, Ph.D Scholars, PG students, Academicians, Industry Personnel, R & D organizations can also attend this seminar.

#### Registration Details

For Industry Personnel/ R&D Organizations: Rs. 7,500/-For Faculty Members/ Academicians: Rs. 3,500/-For Research Scholars/ Associates: For Students: Rs. 1.500/-

\*\*Registration fees include Workshop Kit, Certificate, Lunch, Sna Accommodation can be provided to candidates from outstation at college hostel for additional fees.

#### How to apply?

Interested participants have to mail their details to the below mentioned mail id's on or before 29.10.2018. Selected candidates will be intimated through mail. Registration fees can be paid through DD taken in favour of PRINCIPAL, MVGR College of Engineering or by spot payment

#### Contact personnel

#### Dr. R Maheswaran, Associate Professor maheswaran27@yahoo.co.in

9052 722 221 Mr. Kalyan AVS, Assistant Professor kalyanavs@mvgrce.edu.in 9966 119 507

#### Important dates

Intimation of selected candidates: 29.10.2018 Organized by

3 Day National Workshop on

Hydrologic and Hydraulic

modeling using

MIKE SHE &

MIKE HYDRO RIVER

 $31^{\rm st}$  October –  $02^{\rm nd}$  November, 2018

DHI (India) Water & Environment Pvt. Ltd. Delhi

&

Department of Civil Engineering
MVGR College of Engineering (Autonomous)
Vizianagaram, Andhra Pradesh-535 005
www.mvgrce.com

#### 1. ABOUT THE PROGRAM

Flood prediction and extent mapping is gaining importance over the world due to much evident climate change. Modeling of flood events within any catchment is the starting point to flood prediction and management. This tree day, hands-on course covers the physical and modeling aspects of all the processes in the hydrological cycle. It provides hands-on experience with MIKE SHEE and MIKE HYDRO. Both these tools are widely used for integrated surface water and groundwater modeling (SHE) and flood extent & Risk mapping (FLOOD).

The proposed topics covered:

- a. Introduction to modeling-hydrologic and hydraulic
- b. Types of models and modeling approaches
- c. Background mechanisms and equations adopted
- d. Simulation of hydrologic processes by MIKE SHE and hydraulic routing by MIKE HYDRO
  - e. Model calibration approaches
  - f. Hands-on exercises.

#### 2. RESOURCE PERSONS

Training is provided by Dr.Vikrant Vijay Singh, a senior engineer at DHI Water& Environment, Delhi

#### 3. PROGRAM SCHEDULE

Day-1	
TIME	EVENT
8.30 -9.30	Registration
9.30-10.30	Inaugural function
	including high tea
10.30-12.30	Lecture 1
12.30-1.30	Lunch
1.30-3.30	Lecture 2
3.30-4.30	Hand on experience
Day-2	
8.30 -4.40	Lecture-3 & Hands on
	Exeprience
Day-3	
10.30-12.30	Lecture-4
12.30-1.30	Lunch
1.30-3.30	Hands on experience
3.30-4.30	Valedictory

### 4. LIST OF PARTICIPANTS

			VEAR OF STILIDY KIT & RECEIPT	KIT & RECEIPT	
SL NO	NAME	INSTITUTE/ ORGANIZATION	DESIGNATION	RECEIVED	SIGNATURE
	dr Leeth siù penan Ross	Anothra university College of Chagings Ting	HATECH (FEAM) 2018-2019	Recimied.	ch sarporargo
×	K. Avino h. Ra.	Andhra university	Loth - zors	Recieved	Louist new
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4	B. ALMERAGARAPHE BASI	Andlina University	Asst. Psuferde)	Received	Chuone 6
	S PVENKATESDARA ARD	Andra Unichally, College of Helenta	RESEPTICH JCHOLPK Received	Received	D. Vanhat.
	Priyanka	HT, Madras	Project Associate	Pecieves	Frydanke
	Dr. P. Padmavatti Devi	Dept of Geography one	Des women schange Received	Received	P. du
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1	h. Susingalden	- 14.DT		Recieved	4

DEPARTMENT OF CIVIL ENGINEERING MYGR COLLEGE OF ENGINEEERING (A)

SLNO	NAME	INSTITUTE! ORGANIZATION	YEAR OF STUDY/ DESIGNATION	KIT & RECEIPT RECEIVED	SIGNATURE
1	12 KOLLI RAMISHBU	ANDHRA UNIVERSITY	Research Scholan	Received	X. Rambahn.
na	t3 KUNCHALA KALYANT AND HRA UNINERSITY RESEOONCH Scholas Received. K. Kalyen	AND HRA UNIVERSITY	Research Metodas	Received.	K. Kalyer
3	JH VARRI ALEKHYA	ANDHRA UNINERSTY	M :Tech	Received	V. Alekaya
15	KOSURU GANG NTGSGIRGAN	GNOHRES LUNIVERSITY	11.7c.ch	Received	4.9 Sail
29	V.S. Mendeli Knighter	Andlina conversity	M-Tech	Perewad	Vs. Handy
11	P.DILEEP KUMAR	ANDAKA WINEKSTTY	M.TECH	RECEIVED	P. 434 15
59	Dr. P. Endmaraller Dern	Andlea University	DET WORTH Selections   Recommended		P. P. J.
4	Le Laurence Rive	WAS F	14-9- R. B-	Received	
18	K. Bows mothery	APSAC	Project Assaids	P. ecetroor	Com

DEPARTMENT OF CIVIL ENGINEERING MVGR COLLEGE OF ENGINEEERING (A)

### **5. PHOTOGRAPHS**





## A REPORT ON FACULTY DEVELOPMENT PROGRAM

on

#### POWER ELECTRONICS APPLICATIONS FOR RENEWABLE ENERGY

#### & ELECTRIC VEHICLE APPLICATIONS

04<sup>th</sup> – 09<sup>th</sup> December 2017

Organized by

## **Dept. of Electrical and Electronics Engineering, MVGR College of Engineering,**

in association with

#### **ZUNIK ENERGIES PVT LTD., TIDES-I-2, IITROORKEE**

A 1 WEEK FACULTY DEVELOPMENT PROGRAM WAS ORGANIZED BY EEE DEPT ,WITH ZUNIK ENERGIES BEING THE MAIN RESOURCE PERSONS FOR THE WORKSHOP .The workshop was very well received by the audience comprising of participants across the country.

The topic of the workshop "Power electronics for Electric Vehicles and Renewable Energy applications

#### Theme of the program

The field of Power electronics has been all pervading in its influence especially in the field of renewable and electric vehicle. Power electronics is interdisciplinary in nature and is used in a wide variety of area of Electric Drives, power Quality in power systems and renewable energy systems and electric vehicles etc. This FDP is designed to address applications of power electronics in the industry and to encourage various professionals /students/ academicians towards research and for their academic Quality improvement too. This course will offer a unique opportunity to all the participants in real time power electronic systems and its applications through theoretical sessions and simulation plus laboratory-based experiments and demonstrations.

Apart from that, this FDP will also empower the faculty to be able to make use of the advanced R & D equipment like DSP 28335/28067 kits and also enable them to work on inverter design for a typical solar system.

#### Objectives of the program

- 1. To enable the faculty to gain insight into cutting edge technologies in the field of power electronic applications
- 2. To empower the faculty to gain hands on exposure in using tools (software and hardware) for simulation studies and experiment based research in the above mentioned field
- 3. To create a forum for ongoing interaction with industry and academia in the form of knowledge transfer for collaborative research

#### **Daywise report:**

**Monday(04-12-2017): Monday: An absorbing session on "**DSP 28335 architecture, programming and inverter demonstration Prof S G Kadwane, Nagpur University, really gave the participation an hands on exposure on DSP 28335

**Tuesday**(**05-12-2017**): A lively Discussion on Recent advanced topologies for Renewable Energies Applications (40 Topologies), Mr Raveendhra, CTO, ZUNIK ENERGIES PVT LTD and ZUNIK team ,held the audience spell bound ,where the topic was dealt n great detail with MATLAB simulations and examples.

Wednesday(06-12-2017): The day went further into the topic of Performance investigations and Comparative analysis and also topic CCS for DSP Applications , Mr Raveendhra, CTO, ZUNIK ENERGIES PVT LTD .

**Thursday(07-12-2017):** Mr Raveendhra, CEO, ZUNIK ENERGIES PVT LTD dealt with the topic of Different Modulation Schemes and Xilinx for FPGA Application and Development of Drivers .

**Friday (08-12-2017):** Mr. Raveendhra, CEO, ZUNIK ENERGIES PVT LTD dealt with the topic of Dead band generation and then there was hardware demonstration of Inverter prototype.

**Saturday(09-12-2017):**Last day ,there was session on Power Electronics for Electrical Vehicles", Dr B L Narasimha Raju, NIT, Warangal.

Details of the program:

No of participants: 41

Duration: December4th -9th, 2017









#### AY 2017-18

# Two day National workshop on EDGECAM-2018 27<sup>th</sup> & 28<sup>th</sup> April 2018

#### **About workshop:**

The objective of the workshop is to explain the fundamentals of toolpath creation for CNC machines using Edgecam's revolutionary Workflow software interface. The participants will be guided through hands-on exercises by an experienced instructor to learn various milling and turning operations and corresponding toolpath creation using solid CAD models for single-part machining applications.

#### **Resource persons:**

- Mr. Vignesh Raj, Application Engineer, Applications department, Kriatec Services (p) limited, Chennai, TN
- Mr. K. Ajay, Asst. Prof, Dept of Mech Engg, MVGR College of Engg (A), Vizianagaram, AP

#### **Topics covered:**

- 1. Edge cam software interface
- 2. workflow method explanation in milling & turning
- 3. Milling cycles: Roughing, Profiling, Drilling, Tapping, Chamfering
- 4. Turning cycles: OD roughing, Facing, Finishing, Drilling, Boring, Grooving, Threading

#### Two Day National Workshop on EDGECAM-2018 27th & 28th APR 2018

#### Registration form

Participant's Name :

Designation

Department

Organization

Address for

Communication :

Ph. No

E-mail ID

Registration Details

DD No: Date:

Signature

Amount Rs.:

Note: Please send the registration form along with DD(in favour of Principal, MVGR College of Engineering, payable at Vizianagaram on or before 25th Apr., 2018

Bank:

#### Chief Patron

Dr. P. Ashok Gajapathi Raju Chairman, MANSAS Union Minster for Civil Aviation

Dr.D.R.K. Raju Correspondent, MANSAS

#### Advisory Committee

Dr. K.V.L. Raju, Chairman and Principal

Prof. D.J.J. Ramachandra Raju Vice Principal (Admin)

Dr. Y.M.C. Sekhar

Vice Principal (Academics)

Dr. Ch. Purnachandra Rao Asst. Principal (Academics) Dr. D.R. Prasada Raju Dean (FD)

Dr. R. Ramesh Dean (R&D)

Dr. P Rayindranath Dean (Strategic Planning)

#### Convener

Dr. S. Adinarayana Professor & Head Dept of Mechanical Engr.

Dr. S. Srinivasa Rao Associate Professor Dept of Mechanical Engineering

Assistant Professor Dept of Mechanical Engineering

#### Two Day National Workshop on EDGECAM-2018



27th & 28th Apr 2018



Organized By

#### DEPARTMENT OF MECHANICAL **ENGINEERING**

MVGR COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Re Accredited by (Approved by AIC 15, New Delm, Ke Accredited by NBA, Re Graded 'A' by NAAC, and Permanently Affiliated to JNTU-Kakinada, Listed U/S 2(f) & 12 (B) of the UGC Act 1956) Vizianagaram, Andhra Pradesh, India

#### ABOUT MVGR COLLEGE

Maharaj Vijayaram Gajapathi Raj College of Engineering, Vizianagaram, was established in 1997 under the aegis of MANSAS, an educational trust, founded by Late Dr.P.V.G. Raju, Rajah Saheb of Vizianagaram. The college is permanently affiliated to JNTU, Kakinada, NBA accredited and is given 'A' grade by NAAC.

The campus is located at Chintalavalasa Vizianagaram district of Andhra Pradesh, just 45 minute drive from Visakhapatnam.

The college provides its learning community, the state of art facilities, infrastructure and a competent faculty. The institute encourages collaboration with industry and Institute as a means of reinforcing its curriculum with practical and real world experiences. The college lays emphasis on offering a well-rounder education to mould the students into resourceful

#### TOPICS COVERED

- Edge cam software interface workflow method explanation in milling & turning
- Milling cycles: Roughing Profiling, Drilling, Tapping, Chamfering Turning cycles: OD roughing, Facing, Finishing, Drilling, Boring, Grooving, Threading

#### Contact Address:

#### Dr.S.Adinarayana

Convener, Professor & HOD, Dept of MECH Engg MVGR College of Engineering Chintalavalasa, Vizianagaram Phone: 9440584131 (mobile) Phone: 08922-241038

E-mail: mechhod@mvgrce.edu.in

#### ABOUT DEPARTMENT OF MECHANICAL ENGINEERING

The Department of Mechanical Engineering has been in existence since the inception of the college i.e. 1997. The Department offers Undergraduate Program with an intake of 180 and Post Graduate Program in Product Design and Manufacturing with an intake of 18.

#### HIGHLIGHTS

- · Well qualified faculties with 12 Ph.D.
- Well-furnished laboratories including research labwith state of art equipment.
- Recognized as Research Center by JNTU-K
- Accomplished Research Projects worth more than 65 lakhs sponsored by DST, UGC and AICTE.
- PTC recognized 'Center of Excellence'.
- Tripartite agreements with AP State Government SEIMENS for skill development.
- MOUs with Adriotec, Kriatec Stvices, Zues
- Numerix, PTC. Askar Microns, Dr.Reddy's.
- Runs Add on Programs in Design and

ISNEE, ISHRAE etc.,

- Manufacturing. Recognized Skill Development Center for
- Design, Welding, Refrrigeration and Air-Conditioning & Automotive Training by APSDC. Gold medals for academic excellence from JNTUK
- in 2001, 2013 & 2015. Active participation in competitions organized by professional bodies such as SAE, IE,ISTE, ISIE,

#### RESOURCE PERSONS

1. Mr. Vignesh Raj, Application Engineer, Applications department, Kristec Services (p) limited, Chennai, TN 2. Mr. K. Ajay, Asst. Prof, Dept of Mech Engg, MVGR College of Engg (A), Vizianagaram, AP

#### ORTECTIVE

The objective of the workshop is to explain the fundamentals of toolpath creation for CNC machines using Edgecam's revolutionary Workflow software interface. The participants will be guided through hands-on exercises by an experienced instructor to learn various milling and turning operations and corresponding toolpath creation using solid CAD models for single-part machining applications.

#### REGISTRATION FEE

Faculty Participants: Rs.1000/-

Industrial Participants: Rs.1000/-

Ph D/M.Tech students from Engineering college and University:

Rs: 500/-

(Fee includes Course kit, lunch and refreshments) Last date for receipt of application: 25th Apr 2018



Venue: CAD Lab, Dept of Mech Engg, MVGRCE(A), VZM

### **Schedule:**

Date	Session Timings	Topic s	Resource Person
	10 AM- 12 PM	1. Edge cam software interface	Mr. K. Ajay, Asst. Prof, Dept of Mech Engg,
27-Apr- 18	1.30 PM - 3.30 PM	2. workflow method explanation in milling & turning	MVGR College of Engg (A), Vizianagaram, AP
	10 AM- 12 PM	Milling cycles:  1. Roughing  2. Profiling  3. Drilling  4. Tapping  5. Chamfering	Mr. Vignesh Raj,
28-Apr- 18	1.30 PM - 3.30 PM	Turning cycles: 1. OD roughing 2. Facing 3. Finishing 4. Drilling 5. Boring 6. Grooving 7. Threading	Application Engineer, Applications department, Kriatec Services (p) limited, Chennai, TN

Training Pictures





#### AY 2017-18

### Two day National workshop on EDGECAM-2018- 27<sup>th</sup> & 28<sup>th</sup> April 2018

### DEPARTMENT OF MECHANICAL ENGINEERING MVGR COLLEGE OF ENGINEERING (AUTONOMOUS)

### Department faculty participants list:

S.No	Name	Designation
1.	Sri S Sanyasi Naidu	Asst.Professor
2.	Sri Ch Varun	Asst.Professor
3.	Sri M Y Prasada Rao	Asst.Professor
4.	Sri K Pavan Kumar	Asst.Professor
5.	Sri MD Yousuf Ali	Asst.Professor
6.	Sri G Pramod Kumar	Asst.Professor

## 2018-19

#### REPORT ON NATIONAL WORKSHOP ON HYDROLOGIC AND HYDRAULIC MODELING USING MIKE SHE & MIKE HYDRO RIVER CONDUCTED FROM 31-10-2018 TO 2-11-2018

#### ORGANIZING COMMITTEE

#### CHIEF PATRON

Sri. P Ashok Gajapathi Raju, Chairman, MANSAS Ms. P Aditi Gajapathi Raju, Member, Trust Board

PATRON Dr. KVL Raju, Principal, MVGR

ADVISORY BODY

Dr. DRK Raju, Corres ondent. MANSAS

Dr. YMC Sekhar Vice-Principal

Dr. Ch Purnachandra Rao, Dean (Accreditations & Estt)

Dr P.Ranga Raju Dean(Administration)

Dr P.S.Sitarama Raju Dean(Quality Assurance)

Dr. R Ramesh, Dean (Research & Development)

Dr. K Rajeswara Rao, Dean (Civil Infrastructure) Dr. DR Prasada Raju, Dean (Faculty Development)

CONVENOR Dr. P Markandeya Raju, Professor & HoD

ORGANIZING SECRETARIES
Dr. S Chandramouli, Professor

Dr. R Maheswaran, Associate Professor COORDINATORS

Mr. A Varaprasad, Asst. Professor Mr. Kalyan AVS, Asst. Professor

Dr. Partheepan Ganesan

Mr. S Murali Sagar Varma

Mr. V Vinay

Dr. P Sudheer

Mr. BV Joga Rao

Mr. S Purushotham Rao

Mr. K Santosh Kumar

Ms. T Jahnavi

Mr. B Jagadeesh Mr. A Sai Kumar

Sri. B Ramesh Raji Mr. Ch V Ravi Sankar

Mr. RP Singh

Mr. B Ramu Mr. TP Sreejani

Mr. SSB Sai Kumar

Mr. W Sai Deepak

Mrs. D Praseeda

Mr. G Rahul Reddy Mr. BVSSR Bhaskar

Who can attend?

Faculty Members, Research Scholars, Ph.D Scholars, PG students, Academicians, Industry Personnel, R & D organizations can also attend this seminar.

#### Registration Details

For Industry Personnel/ R&D Organizations: Rs. 7,500/-For Faculty Members/ Academicians: Rs. 3.500/-For Research Scholars/ Associates: For Students: Rs. 1.500/-

on fees include Workshop Kit, Certificate, Lui Accommodation can be provided to candidates from outstation at college hostel for additional fees.

#### How to apply?

Interested participants have to mail their details to the below mentioned mail id's on or before 29.10.2018. Selected candidates will be intimated through mail. Registration fees can be paid through DD taken in favour of PRINCIPAL, MVGR College of Engineering or by spot

Dr. R Maheswaran, Associate Professor maheswaran27@yahoo.co.in

9052 722 221

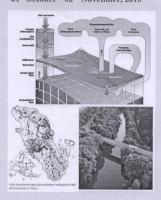
Mr. Kalyan AVS, Assistant Professor kalyanavs@mvgrce.edu.in 9966 119 507

Intimation of selected candidates: 29.10.2018

3 Day National Workshop on Hydrologic and Hydraulic modeling using MIKE SHE &

MIKE HYDRO RIVER

31st October - 02nd November, 2018



Organized by DHI (India) Water & Environment Pvt. Ltd. Delhi

&

Department of Civil Engineering MVGR College of Engineering (Autonomou Vizianagaram, Andhra Pradesh-535 005

#### ABOUT MVGR

Maharaj Vijayaram Gajapathi Raj College of Engineering was established in the year 1997 under the aegis of MANSAS (Maharaj Alak Narayana Society of Arts and Science) founded by Late Dr. PVG Raju, Raja Saheb of Vizianagaram. The college offers UG & PG Engineering Programmes in addition to MBA. Five of the departments have been recognized as a research centers by JNTUK.

The Institute in addition to curricular constantly strives to update its faculty and students with emerging and most recent developments in various disciplines through Industry-Academy interactions by conducting International and National Conferences, Seminars, Add-on Courses and Guest Lectures. The Institute has 20 sponsored research projects from DST, AICTE, UGC, IEI and ICSSR, The Institute has student chapters of professional bodies such as IEI, IEEE. Recognized as "Center of Excellence" by PTC for Creo and Windchill training, VIRTUSA for PEGA training, MICROSOFT under Ed-vantage Program, WIPRO for its WIPRO MISSION 10X Technology Learning Center (MTLC), TCS for its Academic Interface Program (AIP), SIEMENS for Digital Manufacturing. The college is recognized as a "Business Incubator" by the Ministry of Micro, Small and Medium Enterprises (MSME), Government of India and Skill Development Center by AP State Skill Development Corporation.

#### ABOUT THE DEPARTMENT

Department of Civil Engineering was established in the year 2009 offers B.Tech in Civil Engineering and M.Tech in Structural Engineering. The UG program is accredited by NBA. The Department has state of the art infrastructure & well-equipped laboratories. 26 well qualified strong team of faculty with a blend of industry, R&D and teaching experience with varied specializations offer teaching-learning services. Among them 6 are

orates, and 10 are currently pursuing their doctoral research. The department has a dedicated Computer Centre & Research Centre promoting active research. The department has certified competency for skill development leading to award of certification in Remote Sensing and STAAD.Pro in collaboration with Indian Institute of Remote Sensing (IIRS) and Bentley. The faculty of the department have more than 100 quality research publications in journals of international repute. The department has funded research project worth more than 200 Lacs from DST. The department offers consultancy services to various Government and Private organizations like GVMC, Vizianagaram Traffic Police, AP Road Safety Department, CPWD, Visakhapatnam Urban Development Authority, SPCB Vizianagaram. The department serves as a Third-party QA &QC agency for Vizianagaram Municipality.

#### ABOUT MIKE SHE & MIKE HYDRO

MIKE Powered by DHI is a part of DHI, the global organization dedicated to solving challenges in water. environments worldwide. It provides a range of software products that enables to accurately analyze, model and simulate any type of challenge in water environments such as oceans and coastlines, rivers and reservoirs, ecology, groundwater, water distribution, wastewater and many

MIKE SHE provides truly integrated modelling of groundwater and its movement, surface water, recharge and evapotranspiration. It provides a seamless integration of all the important processes of the hydrological cycle.

MIKE HYDRO MIKE HYDRO River is top quality river modelling, covering more application areas than any other river modelling package available. It enables you to model a variety of tasks related to river hydraulics, water quality, flooding, forecasting, navigation as well as catchment dynamics and runoff. Applications of MIKE SHE range from classical flood extent and risk mapping environmental impact assessments of severe flood ev

#### ABOUT THE WORKSHOP

Flood prediction and extent mapping is g importance over the world due to much evident of change. Modeling of flood events within any catchin the starting point to flood prediction and manage This three-day, hands-on course covers the physic modelling aspects of all the processes in the hydro cycle. It provides hands-on experience with MIKE and MIKE HYDRO. Both these tools are widely us integrated surface water and groundwater modern and groundwater mod (SHE) and flood extent & risk mapping (FLOOD).

The proposed topics covered:

- Introduction to modeling hydrologic and hydr
- Types of models and modeling approa
- Background mechanisms and equations adopted
- Simulation of hydrologic processes by MIKE SH hydraulic routing by MIKE HYDRO.
- Model calibration approaches. Hands-on exercises

#### RESOURCE PERSON

Training is provided by **Dr. Vikrant Vijay Si** senior engineer at DHI Water & Environment, Dell

Dr. Vikrant Vijay Singh is a PhD in Environ Science with specialization in Groundwater resource interest lies in numerical modeling of surfagroundwater flow and has conducted training sessi groundwater modeling and manage ment c organized for state departments.

#### 1. ABOUT THE PROGRAM

Flood prediction and extent mapping is gaining importance over the world due to much evident climate change. Modeling of flood events within any catchment is the starting point to flood prediction and management. This tree day, hands-on course covers the physical and modeling aspects of all the processes in the hydrological cycle. It provides hands-on experience with MIKE SHEE and MIKE HYDRO. Both these tools are widely used for integrated surface water and groundwater modeling (SHE) and flood extent & Risk mapping (FLOOD).

The proposed topics covered:

- a. Introduction to modeling-hydrologic and hydraulic
- b. Types of models and modeling approaches
- c. Background mechanisms and equations adopted
- d. Simulation of hydrologic processes by MIKE SHE and hydraulic routing by MIKE HYDRO
  - e. Model calibration approaches
  - f. Hands-on exercises.

#### 2. RESOURCE PERSONS

Training is provided by Dr.Vikrant Vijay Singh, a senior engineer at DHI Water& Environment, Delhi

#### 3. PROGRAM SCHEDULE

Day-1	
TIME	EVENT
8.30 -9.30	Registration
9.30-10.30	Inaugural function
	including high tea
10.30-12.30	Lecture 1
12.30-1.30	Lunch
1.30-3.30	Lecture 2
3.30-4.30	Hand on experience
Day-2	
8.30 -4.40	Lecture-3 & Hands on
	Exeprience
Day-3	
10.30-12.30	Lecture-4
12.30-1.30	Lunch
1.30-3.30	Hands on experience
3.30-4.30	Valedictory

### 4. LIST OF PARTICIPANTS

SLNO	NAME	INSTITUTE/ ORGANIZATION	SLNO NAME INSTITUTE/ORGANIZATION YEAR OF STUDY! KIT & RECEIPT SIGNATURE	KIT & RECEIPT	SIGNATURE
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-	S PVENKATESJARA ARD	Andra Variousty, College of Heavile	RESERVED JUNGAR RECEIVED	Received	D. Vanhat.
1	Priyanka	NT, Modras	Project Associate	Recieves	A Marike
3-	Dr. P. Padmavatti Devi	Dept of Geography forth	DES wower Livans Received	Received	Podu
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DEPARTMENT OF CIVIL ENGINEERING MVGR COLLEGE OF ENGINEEERING (A)

SLNO	NAME	INSTITUTE/ ORGANIZATION	YEAR OF STUDY/ DESIGNATION	KIT & RECEIPT RECEIVED	SIGNATURE
1	X KOLLI RAMBABU	ANDHRA UNIVERSITY	Research Scholan	Received	X. Rambahar
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DEPARTMENT OF CIVIL ENGINEERING MYGR COLLEGE OF ENGINEERING (A)

### **5. PHOTOGRAPHS**





#### **MVGR**

Maharaj Vijayaram Gajapathi Raj College of Engineering (Autonomous) was established in the year 1997 under the aegis of "MANSAS" educational trust, founded by the Raja Saheb of Vizianagaram, Dr. P.V.G. Raju in 1958. The college is permanently affiliated to JNTUK, Kakinada. All its UG programs accredited by NBA and the Institution is certified with grade 'A' by NAAC.

The college is located at Chintalavalasa, Vizianagaram district, Andhra Pradesh, 45 minutes drive from Visakhapatnam.

The college provides its learning community, the state-of-art facilities, infrastructure and a competent faculty. The Institute encourages the collaboration with Industry and Institute as a means of reinforcing its curriculum with practical and real world experiences. The college lays emphasis on offering a well-rounded education to mould the students into resourceful engineers.

#### **Department of EEE**

The department offers B.Tech and M.Tech.(Power systems). It has qualified and experienced faculty to make the students well rounded professionals.

#### **Salient Features**

- Accredited by NBA.
- Recognized as research center, which can offer Ph.D. programs.
- Collaborated with SIEMENS Ltd for training the students with certification programs on PLC Automation and AC/DC Drives.
- Equipped with software tools like MATLAB, LabVIEW and PSCAD.
- Has advanced equipment to encourage students and faculty towards research.

### Theme of the Program

In recent years, Power Electronic converters are widely used to control the flow of power in power system applications. Power electronic applications are growing rapidly in industrial, commercial, residential, transportation, utility, aerospace, and military environments. Hence, there is a need to update recent advances in the field of power electronics. In this context, this FDP is proposed which aims at providingan opportunity to faculty members, research scholars and post graduate students to know the latest developments in the field of Power Electronics and the possible areas of research.

#### **Objective of the Program**

This program will give an exposure to the participants on contemporary developments in the field of Power Electronics.

#### This program provides an insight into:

- Recent Trends in Power Electronics for Sustainable Energy
- Grid connected solar and wind energy systems
- Application of power electronics in Smart Grid
- Quick charging energy Storage Systems
- Control of power flows in power systems
- Power electronics for Power Quality
- Electric Vehicles and Smart Urban Transportation

#### **Resource Persons**

Dr A. K Panda NIT, Rourkela.

Dr K Shiva Kumar IIT, Hyderabad.

Dr C N Bhende IIT, Bhubaneswar.

Dr A Jayalaxmi JNTUH, Hyderabad.

Dr P Chandra Sekhar IIT, Bhubaneswar.

Dr G Gurunath IISC, Bangalore.

Dr N Jayaram NIT-AP.

#### Convener

Dr. Sarat Kumar Sahu, Professor and Head

#### **Co-Conveners**

Dr. R. Gowrisankara Rao, Professor

Mr. K.S. Ravi Kumar, Associate Professor

Mr. I.Kranthi Kiran, Associate Professor

Mr. M. Venu Madhav, Associate Professor

#### Coordinator

Mr. Ch. Bhavanisankar, Assistant Professor

Phone: +91 9492544880, +918922 241167(O)

email: peaps2k18@mvgrce.edu.in

College website: www.mvgrce.edu.in

#### **Organizing Committee**

Faculty of EEE Department.

#### Venue

Department of EEE, MVGR College of Engg. (A).

#### Registration fee

For Faculty of Engg. Colleges
Rs. 1000/For Industry participants
Rs. 2000/For Student participants
Rs. 500/-

Registration fee is payable through demand draft drawn in favor of Principal, MVGR College of Engineering, Vizianagaram on or before 12<sup>th</sup>December 2018. The registration form can be downloaded from the college web site and the filled-in form can be sent to the above mentioned email.

### Accommodation

For external participants, accommodation will be provided.

Page 67 of 83

## REGISTRATION FORM Participant's Name Designation Department Organization Address Phone no Email ID Accommodation required : Yes/No **Registration details** DD No.: Date: Amount in Rs. : Bank: Signature of the participant **Sponsorship Certificate** Certified that Mr./Ms./Mrs. is a faculty/employee of our institution /industry and is hereby sponsored to attend Faculty Development Program on "Power Electronic Applications in Power Systems (PEAPS-2k18)" at MVGR College of Engineering, Vizianagaram from 17th to 22nd December 2018. Place: Date: Signature of sponsoring

Authority with seal

#### **Chief Patron**

Sri. P. Ashok Gajapathi Raju

Chairman, MANSAS

#### **Patrons**

Sri. C. Kutumba Rao

Vice-Chairman, Planning Commission, A.P.

Dr. I. V. Rao

Former Vice-Chancellor, NTR Health University

Ms. P. Aditi Gajapathi Raju Member, MANSAS Trust Board

Dr. K.V.L.Raju

Correspondent, MANSAS

and Principal, MVGR College of Engineering (A).

#### **Advisory Board**

Prof. D.J.J. R.Raju, Advisor, EEE Dept.

Dr. Y.M.C. Sekhar, Vice-Principal (Academics)

Dr. D.R. Prasada Raju, Dean (Faculty Development)

Dr. P. Ravindranath, Dean (Strategic Planning)

Dr. M. Sunil Prakash, Dean (Training &Placements)

Dr. K. Rajeswara Rao, Dean (Civil Infrastructure)

Dr. R. Ramesh, Dean (Research & Development)

Prof. P.S.S. Sitharama Raju, Dean (QA)

Dr. S. Srinivasa Rao, Dean (Student Affairs)

Dr. T.V.N. Partha Sarathi, Dean (Examinations)

Dr. Ch. Purnachandra Rao, Dean (A & E)

Sri. P. Ranga Raju, Dean (Administration)

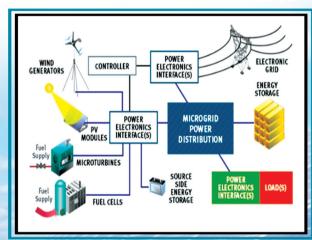




## A Faculty Development Program on

# POWER ELECTRONIC APPLICATIONS IN POWER SYSTEMS ( PEAPS-2k18 )

From 17th to 22nd December 2018



### Organized by

## Dept. of Electrical & Electronics Engineering MVGR College of Engineering (Autonomous)

(Approved by AICTE, New Delhi, Accredited by NBA of AICTE, Graded 'A' by NAAC of UGC and Permanently Affiliated to JNTUK)

Chinthalavalasa, Vizianagaram - 535005, Andhra Pradesh Visit us at: www.mvgrce.edu.in

Visit us at: www.mvgrce.edu.in email : peaps2k18@mvgrce.edu.in



#### A Report

on

One Week Faculty Development Program
"Power Electronics Application to Power Systems"
during
17<sup>th</sup> to 22<sup>nd</sup> December 2018
organized by

## DEPARTMENT OF EEE MVGR COLLEGE OF ENGINEERING (AUTONOMOUS)

(Listed Under 2(f), 12(b) Act of UGC)

(Permanently Affiliated to JNTU, Kakinada, and Approved by AICTE & NBA, New Delhi, Accredited with "A" grade by NAAC) VIZIANAGARAM

In recent years, Power Electronic converters are widely used to control the flow of power in power system applications. Power electronic applications are growing rapidly in industrial, commercial, residential, transportation, utility, aerospace, and military environments. Hence, there is a need to update recent advances in the field of power electronics. In this context, this FDP is proposed which aims at providing an opportunity to faculty members, research scholars and post graduate students to know the latest developments in the field of Power Electronics and the possible areas of research.

#### Objective of the Program

This program intends to provide the exposure on contemporary developments in the field of Power Electronic Controllers. It gives an opportunity to the faculty and the researchers to update themselves.

#### This program provides an insight into

- \* Recent Trends in Power Electronics for Sustainable Energy
- Grid connected solar and wind energy systems
- Application of power electronics in Smart Grid
- Quick charging energy Storage Systems
- Control of power flows in power systems
- Power electronics for Power Quality
- ❖ Electric Vehicles and Smart Urban Transportation

#### Day wise report:

**Monday(17-12-2018):** IN the Inaugural sessions ,Key note address was given by Chief guest, Sri **ASV Ramanan**, Executive Director,HPCL on importance and application of smart grid and green energy. The days session was to be taken up by Dr A JayaLaxmi ,Professor,EEE Dept,JNTU HYD , on the Challenges and advances of Power Electronics for future applications". But due to cyclone the day's session could not be held .

**Tuesday(18-12-2018)**: The session was given on Power Converters for Renewable Energy Applications and Battery Charging Systems by Mr. Adiseshanaik, AGM, Visakha Steel plant, Visakhapatnam. After that, the topic of Modeling of Renewable Energy Systems was given by Dr. A. K. Panda, NIT-Rourkela, Rourkela.

Wednesday(19-12-2018) The session was given on Phase controlled converters and PWM converters and Dr. K. Siva Kumar, Professor, Electrical Engineering Dept. IITH, Hyderabad.

After that the session on Grid integrated Power Converters was given by Dr. N. Jayaram, Asst. Professor, EEE Dept., NIT-AP., Thadepallegudem.

**Thursday (20-12-2018)** The session was given in morning on Control Techniques for STATCOM and UPQC and afternoon on Simulation Techniques in Power electronics applications by Dr. C. N. Bhende, Associate professor ,School of Electrical Sciences, IIT Bhubaneswar, Bhubaneswar. It was well taken by the audience with interaction with speaker.

Friday (21-12-2018): The session was given in morning on Role of converters in Electric Vehicles by Dr. P. Chandrasekhar, Asst. Professor, School of Electrical Sciences, IIT Bhubaneswar, Bhubaneswar.

**Saturday (22-12-2018)**: It was very interesting day in terms of the learning experience for audience. Dr. G. Gurunath, Asst. Professor, Dept. of Electrical Engineering, IISc, Bangalore.

The topics were very different and fundamental "Problem identification in the area of Power Electronics "and "Conversion of research work into transactions"

The workshop seemed to attract a favorable opinion among the audience as per the attendance and feedback, and overall it can be deemed to be a grand success as it fulfilled the set objectives and achieved its outcomes.

Day	Time	Event	Resource Person Details
Monday	9:30- 10:45	Inaugural session	
	10:45-11:00	Tea Break	
	11:00-01:00	Key note address by chief guest on importance and application of Smart Grid and Green Energy.	Chief guest: K. Srihari DGM, Power Grid Corporation of India
17-12-2018	01:00-02:00	Lunch Break	
17-12-2016	02:00-03:00	Power Electronic Applications in Power Systems – Overview.	Dr. A. Jaya Lakshmi
	03:00-03:15	Tea Break	Professor
	03:15-04:30	Power Electronic Applications in Power Systems – Scope of research.	EEE Dept. JNTUH, Hyderabad.
Tuesday 18-12-2018	9:30- 11:00	Introduction to Power semiconductor devices.	Mr. Adi Sesha Naik
	11:00-11:15	Tea Break	AGM
	11:15-01:00	Applications of Drive control system (DC and AC) in process industries	Visakha Steel plant, Visakhapatnam.
	01:00-02:00	Lunch Break	

	02:00-04:30	Topological Issues in Single Phase Power Factor Correction	Dr. A. K. Panda NIT-Rourkela Rourkela.
	9:30- 11:00 11:00-11:15 11:15-01:00	Role of power electronics in renewable applications.  Tea Break contd Role of power electronics in renewable applications	Dr. K. Siva Kumar Professor Electrical Engineering Dept. IITH, Hyderabad
Wednesday 19-12-2018	01:00-02:00	Lunch Break Current Research in Micro grids	Tryderabad
19-12-2018	02:00-04:30	and Smart Grids: Opportunities & Challenges &Current Research in Electric Vehicles: Opportunities & Challenges	Dr. V. Sandeep Asst. Professor EE Dept. CU of Karnataka Gulbarga.
	9:30- 11:00	Integration of Large Wind Power to the Grid	Dr. C. N. Bhende
Thursday	11:00-11:15 11:15-01:00	Tea Break Islanding detection scheme for distributed generation systems using modified reactive power control strategy	Associate professor School of Electrical Sciences IIT Bhubaneswar, Bhubaneswar.
20-12-2018	01:00-02:00	<u> </u>	
	02:00-04:30	Optimal Power Flow Management of Grid Connected Photovoltaic-Battery System & Modified Jaya Algorithm for Optimization.	Dr. C. N. Bhende Associate professor School of Electrical Sciences IIT Bhubaneswar, Bhubaneswar.
	9:30- 11:00	Integration of Renewable Energy Sources to Grid-Challenges	Dr. P. Chandrasekhar Asst. Professor
Friday	11:00-11:15 11:15-01:00	Tea Break contdIntegration of Renewable Energy Sources to Grid- Challenges	School of Electrical Sciences IIT Bhubaneswar, Bhubaneswar.
21-12-2018	01:00-02:00	Lunch Break	
	02:00-04:30	Application of Power Electronics in Smart Grid-Stability	Dr. P. Chandrasekhar Asst. Professor School of Electrical Sciences IIT Bhubaneswar, Bhubaneswar.
Saturday 22-12-2018	9:30- 11:00	Problem identification in the area of Power Electronics	Dr. G. Gurunath Asst. Professor

11:00-11:15	Tea Break	Dept. of Electrical
11:15-01:00	Conversion of research work into	Engineering,
	transactions	IISc, Bangalore.
01:00-02:00	Lunch Break	
02:00-04:00	Valedictory	

### 5-day Faculty Development Program on Hybrid and Electric Vehicle Technologies (HEVT '2018)

## 11<sup>th</sup> to 15<sup>th</sup> December 2018

### **About the Workshop:**

Although internal combustion engines have been used for more than a century, significant improvements in engine combustion, thermal efficiency and emissions reduction are still possible. The objective of the 5-day faculty development program on "Hybrid and Electric Vehicle Technologies (HEVT '2018)" will be to discuss an overview of current advances in automotive and emission reduction technologies in line with Bharat stage VI norms. The program will also cover the basics of Hybrid and Electric vehicle technologies and Challenges in development of Electric vehicle battery systems. This 5-day faculty development program provides the platform for faculty to interact with experts from academia and industry in the area of Hybrid and Electric vehicle technology. It will also help the faculty to enhance their knowledge and to do research with modern tools and technologies.

### **Resource persons:**

- 1. Dr N.H. Walke, Deputy Director, Powertrain Engineering, ARAI, Pune
- 2. Dr. Simhachalam Juttu, Deputy General Manager, Powertrain Engineering, ARAI, Pune
- 3. Dr.M.V. Uchgoankar, Head (HR) & Deputy Director, ARAI Pune
- 4. Dr.M. Venkateswarlu, AGM-Technology Centre, Amara Raja Batteries Ltd
- 5. Prof.K.Munshi, Director, Industrial Design Centre, IIT Bombay
- 6. Prof.C.S.Shankar Ram, Professor in Engineering Design, IIT Madras
- 7. Mr.Suroju Ramakrishnan, Manager R&D, Renault Nissan
- 8. Mr.Rajendra Khile, Dy.General Manager, Product Engineering, Renault Nissan
- 9. Mr. Yogesh Nagendran, Team leader, Renault Nissan
- 10. Mr. Jothi Prasad Shette, Manager, Cummins India Ltd
- 11. Mr.G.Nischal, Design Engineer, R&D, Mercedes Benz

### **Topics covered:**

- Challenges in development of Hybrid and Electric vehicles
- Future of Hybrid and Electric Vehicles in India
- Electric vehicle battery technologies and advancements
- Types of power trains-control a hybrid powertrain
- Testing of Hybrid and electric vehicles

### • Advanced IC engine and emission control Technologies to meet BS VI Norms

### ABOUT MVGR COLLEGE

Maharaj Vijayaram Gajapathi Raj College of Engineering, Vizianagaram, was established in 1997 under the aegis of MANSAS, an educational trust, founded by Late Dr.P.V.G. Raju, Rajah Saheb of Vizianagaram. The college is permanently affiliated to JNTU, Kakinada, NBA accredited and is certified with 'A' grade by NAAC.

The campus is located at Chintalavalasa. Vizianagaram district of Andhra Pradesh, 45 minutes drive from Visakhapatnam.

The college provides its learning community, the state of art facilities, infrastructure and a competent faculty. The Institute encourages collaboration with industry and Institute as a means of reinforcing its curriculum with practical and real world experiences. The college lays emphasis on offering a well-rounded education to mould the students into resourceful engineers.

### ABOUT THE DEPARTMENT

The Department of Mechanical Engineering was established in 1997 and offers undergraduate program with an intake of 180, Post graduate Program in Product Design and Manufacturing and Ph.D Programs. The salient features of the Departments includes,

- · Well qualified faculties with 13 Doctorates
- Well furnished laboratories including research labs with state of art equipment
- · Recognized as Research Center by JNTU-K
- Accomplished / ongoing Research Projects worth more than 1.5 Crore sponsored by DST, UGC and AICTE.
- · CM Centre of Excellence for 3Ds by Dassault Systems
- · AP Skill Development Centre by Siemens
- Certificate Programs on CREO, Wind Chill and Edge CAM

### OBJECTIVE OF THE WORKSHOP

Although Internal Combustion Engines are in usage for more than a Century, there is an ample scope for improvements in engine combustion, thermal efficiency and emissions reduction by adopting new technologies. The objective of this faculty development program on "Hybrid and Electric Vehicle Technologies (HEVT '2018)" is to cast light on current advances in automotive and emission reduction technologies in linewith BS VI norms. The program also covers the basics of Hybrid and Electric vehicle technologies and Challenges in the development of Electric vehicles. The 5-day faculty development program provides a platform for faculty to interact with experts from industry and academia. It will also help the faculty enriching their domain knowledge and provide necessary inputs for future research.

### TOPICS TO BE COVERED

- Advanced IC engine Technologies
- Challenges in development of Hybrid and Electric vehicles
- Future of Electric Vehicles in India
- Electric vehicle power train technologies
- Introduction to BS VI Norms and Exhaust emission control technologies
- Battery Management System



### RESOURCE PERSONS

- Prof.K.Munshi, Former Head of IDC, IIT Bombay, Director, CTech Labs Pvt. Ltd.,
- 2. Dr.N.H.WALKE, Deputy Director, ARAI, Pune
- 3. Dr.M.V.Uchgoankar, Head (HR) & Deputy Director, ARAI
- 4. Dr.SIMHACHALAM JUTTU, DGM, ARAI, Pune
- 5. Mr.Suroju Ramakrishnan, Manager (R&D), Renault Nissan
- 6. Mr.Jothi Prasad Shette, Manager-Cummins India Limited
- 7. Dr.M. Venkateswarlu, AGM, Amara Raja Batteries Ltd
- Prof C.S. Shankar Ram, Dept. of Engineering Design, IITM
   Mr.Nischal G, Sr. Design Engineer (R&D), Mercedes Benz
   Mr. Rajendra Khile, DGM, Renault Nissan
- 11. Mr. Yogesh Nagendran, Team Leader, Renault Nissan
- 12. Mr. Nagarajan Kannan, MD, Dharma Sai Motors, Chennai
- 13. Mr. Rakesh Mulik, Manager, ARAI, India

### REGISTRATION FEE

Rs. 2000/- for Industry and Faculty Participants Rs: 1000/- for Ph.D/M.Tech students from Engineering colleges and Universities: (Fee includes Course kit, lunch and refreshments)

Last date tor Registration: 7th December 2018.

### BOARDING AND ACCOMMODATION

For outside participants, accomodation will be provided in Boys hostel on payment basis.



5-day Faculty Development Program on Hybrid and Electric Vehicle Technologies (HEVT '2018)

11th to15th, Dec 2018
DEPARTMENT OF MECHANICAL ENGINEERING
MVGR COLLEGE OF ENGINEERING (A)
VIZIANAGARAM AP

### Registration Form

Participants Name :

Designation

Department

Organization

Address for Communication

Ph. No

E-mail ID :

Registration Details :

DD No Date :

Amount Rs. : Bank :

Signature of the Candidate

Note: Please send the registration form along with DD (in favour of Principal, MVGR College of Engineering (A), payable at Vizianagaram on or before 7th, December 2018

### Chief Patron

Sri P. Ashok Gajapathi Raju Chairman, MANSAS

Ms. P. Aditi Gajapathi Raju Member, MANSAS Trust board

Patron

Dr. K.V.L. Raju,

Correspondent, MANSAS and Principal, MVGR College of Engineering (A)

### Advisory Committee

Dr. Y.M.C. Sekhar

Vice Principal (Academics)
Mr. P. Ranga Raju

Dean (Admin)

Dr. D.R. Prasada Raju

Dean (FD)

Dr. R. Ramesh

Dean (R&D)

Dr. P.Ravindranath

Dean (Strategic Planning)

### Convener

Dr. S. Adinarayana

Professor & Head Dept of Mechanical Engg. Mobile : 9440584131

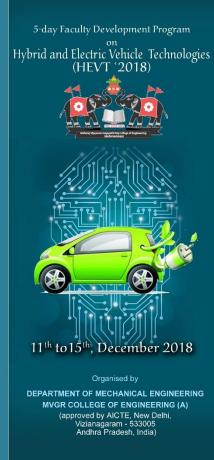
Phone: 08922-241038 E-mail: mechhod@mvgrce.edu.in

### Coordinators

Dr. N. Ravi Kumar Professor Mobile: 9949618492

Dr. M.K. Naidu

Associate Professor Mobile : 9866474301





### DEPARTMENT OF MECHANICAL ENGINEERING MVGR COLLEGE OF ENGINEERING (A): VIZIANAGARAM 5-day Faculty Development Program on Hybrid & Electric Vehicle Technologies (HEVT<sup>4</sup>2018) 11<sup>th</sup> - 15<sup>th</sup> December 2018



Date	Time	Session	Resource Person	Topic Name	Venue	Target Audience
Day 1 (11.12.2018)	9:15 - 10.00		Regist		rudience	
	10.00 - 10.30		Inaugural	Admn. Seminar	Participants, Faculty, M Tech I semester students and III A, B,C students	
	10.30 -12:00	01	Prof. K.Munshi, Former H Director, CTech Labs Pvt. I Sustainable EV Mobility I	Hall		
	12:00 -1.30	SELVE S	Lun	8		
	1.30 - 03:30	02	Dr. Somnath Sengupta, Ass Electric Vehicles (xEvs)	Mech. Conference Hall		
_	9:30 - 11:00	03	Mr. Yogesh Nagendrian, Ma		Participants Faculty, M Tech I semester	
816	11:00 - 11: 15	ESCH TOP S	High			
Lag	11:15 -12:45	04	Mr.Rajendra Khile, Dy.Gen Renault Nissan, Chennai EV Charging Systems	Mech.		
	12:45 -02:15		Lu	Conference Hall		
	02:15 - 03:45	05	Mr. G.Nischal Current status of Electric & &Potential scope of autom		students	

Date	Time	Session	Resource Person	Topic Name	Venue	Target Audience	
018)	9:30 - 11:00	06	Prof.C.S.Shankar Ram, P. IIT Madras. Longitudinal Dynamics Vehicles		Participants,		
12.2	11:00 - 11: 15		Hig	gh Tea	Mech. Conference	Faculty,	
Day 3 (13.12.2018)	11:15 -12:45	07	Dr.M.Venkateswarlu, AC Centre, Tirupati Lithium-ion Battery Ch	Hall	M Tech 1 <sup>st</sup> semester students		
Da	12:45 -02:15			unch			
	02:15 - 03:45	08	Mr.Suroju Ramakrishna Electrical Vehicles for U	, Manager R&D, Renault Nissan Irban Mobility			
Day 4 (14.12.2018)	9:30 - 11:00	09	Mr.Nagraj Kannan, MD,D Electric mobility Implem		Participants Faculty, M Tech 1st semester students		
12.	11:00 - 11: 15	REAL PROPERTY.	Hig	Mech. Conference			
(14.	11:15 -12:45	10	Dr.Simhachalam Juttu, De	Hall			
ay 4	12:45 -02:15		L				
Ď	02:15 - 03:45	11	Mr. Rakesh Mulik, Manag	ger, ARAI, Pune			
	9:30 - 11:00	12	Dr N.H. Walke, Deputy D Advances in IC engine p	Director, ARAI, Pune Dower train Technologies		Participant	
	11:00 - 11: 15			Mech. Conference	Faculty,		
Day 5 (15.12.2018)	11:15 -12:45	13	Dr.M.V. Uchgoankar, Hea	Hall	M Tech 1s semester students		
	12:45 -02:15		Lu				
	02:15 - 03:30		Vale	Mech. Conference Hall	Participant Faculty, M Tech 1 <sup>th</sup> semester students		





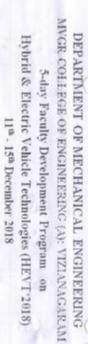
### 5-day Faculty Development Program on Hybrid and Electric Vehicle Technologies (HEVT '2018)

# DEPARTMENT OF MECHANICAL ENGINEERING MVGR COLLEGE OF ENGINEERING (AUTONOMOUS)

### Department faculty participants list:

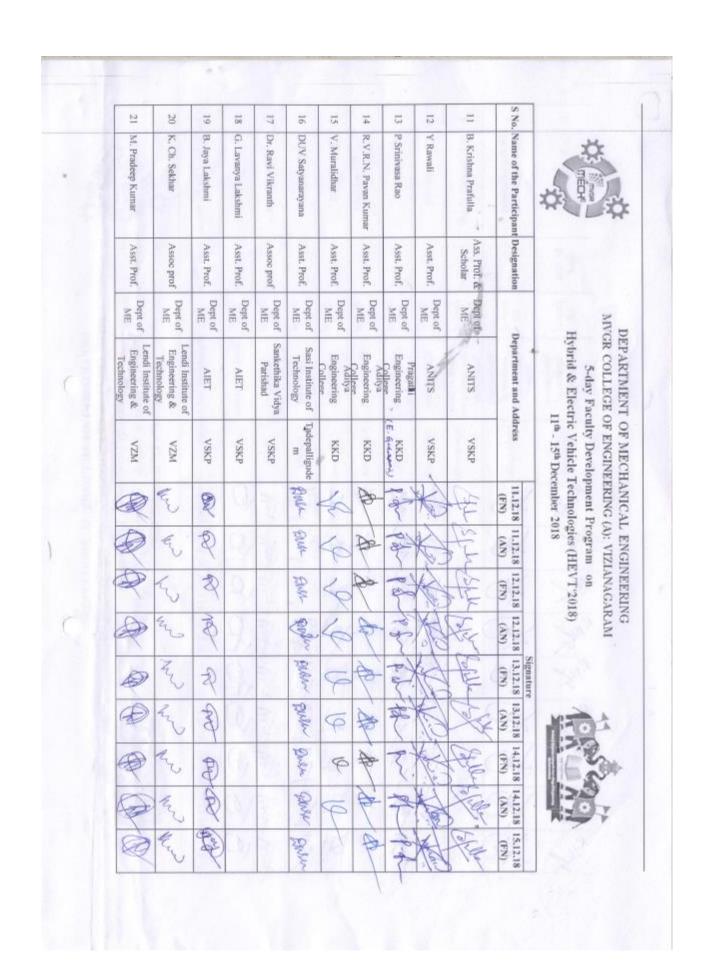
Sno	Name	Designation					
1.	Dr S Adinarayana	Professor					
2.	Dr N Ravi Kumar	Professor					
3.	Dr I Sudhakar	Assoc.Prof					
4.	Sri K Praveen	Assoc.Prof					
5.	Dr M K Naidu	Assoc.Prof					
6.	Sri B A Ranganath	Assoc.Prof					
7.	Dr S Srinivasa Rao	Assoc.Prof					
8.	Sri M Anil Prakash	Assoc.Prof					
9.	Dr M Siva Subrahmanyam	Assoc.Prof					
10.	Dr R S Umamaheswara Raju	Assoc.Prof					
11.	Sri K Ajay	Asst.Prof					
12.	Sri S Sanyasi Naidu	Asst.Prof					
13.	Sri Ch Varun	Asst.Prof					
14.	Dr B Madhava Varma	Sr.Asst.Prof					
15.	Sri G Satyanarayana	Asst.Prof					
16.	Sri G Rajesh	Asst.Prof					
17.	Sri M Y Prasada Rao	Asst.Prof					
18.	Sri G Veeraiah	Asst.Prof					
19.	Sri K Pavan Kumar	Asst.Prof					
20.	Sri B Srinivas	Asst.Prof					
21.	Sri MD Yousuf Ali	Asst.Prof					
22.	Sri G Pramod Kumar	Asst.Prof					
23.	Sri S Joshua Kumar	Asst.Prof					
24.	Sri N Jagadeesh	Asst.Prof					
25.	Sri D Sri Harsha	Asst.Prof					
26.	Sri D Santhosh Kumar	Asst.Prof					
27.	Sri J V Sandeep	Asst.Prof					
28.	Sri P Sreenu	Asst.Prof					
29.	Sri S Anil Kumar	Asst.Prof					
30.	Sri Aditya Rathore	Asst.Prof					







0	9	-00	7	6	5	4	w	12	-	SNo	
Dr. R. Prithivirajan	Dr. Rajendraprasad Name	Sk.Hidayatulla Shariff	A Radha Krishna	M Gangaraju	Gari Suryachandra Swamy	T Rama Krishna	B. Satya Krishna	A. Venkata Swami	Dr. P Ugandhar	S.No. Name of the Participant Designation	
Sr. Asst. Prof.	Assoc prof	Assoc prof	Scholar	Asst. Prof.	Scholar	Asst. Prof.	Asst. Prof.	Assoc. Prof.	Sr. Lecturer	Designation	
Dept of ME	Dept of EE	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME		
Madamapaile Institute of Technology & Science	Madanapalle Institute of Technology & Science	Avanthi Institute of Engineering and Technology	KL University	Vidyanikethan Ensineerine	KL University	Sri CRRCoE	Sri Vishnu Engineering College	GNITC	Govt. Polytechnic	Department and Address	
Madanapalle	Madanapalle	Tagarapuvalas a	Vijayawada	Tirupati	Vijayawada	Eluru	BMVM	НУД	VZM	dress	
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# DEPARTMENT OF MECHANICAL ENGINEERING MYGR COLLEGE OF ENGINEERING (A): VIZIANAGARAM 5-day Faculty Development Program on Hybrid & Electric Vehicle Technologies (HEVT 2018) 11th - 15th December 2018



33	32	131	30	29	28	27	26	13	24	23	22	S No.	
Salagrama Narasimba Abhinav	Nadikoppula Pardhasaradhi	Marisetti Manikantha Nani Suresh	Korupuru Thirumala Sai Srinivas	Injarapu Kamal Kumar	Gandreti Chaitanya Srinivasa Gopi Bharat	Dokala Sravani	Danda Krupa Nirmala Swaroopa Rani	Bogala Ashok	Aggunna Hima Bindu	Adurthi Sai Krishna	Ramu Garugubilli	S No. Name of the Participant Designation	
P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	P.G Student	Asst. Prof.	Designation	
Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of ME	Dept of, ME	B	
MVGRCE	MVGRCE	MVGRCE	MVGRCE	MVGRCE	MVGRCE	MVGRCE	MVGRCE	MVGRCE	MVGRCE	MWGRCE	ME Engineeing	Department and Address	4
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# 2019-20

### AY-2019-2020

# Professional Development/Administrative Trainings of Non-Teaching Staff

### DEPARTMENT OF MECHANICAL ENGINEERING

### **MVGR COLLEGE OF ENGINEERING (A)**

Name of the program	Resource person	Members attended	Date	
Training on Lathe m/c	SrI. G. Pramod MVGRCE (A)	Sri K.Prasada Rao Sri VPrasada Rao Sri B.Linga Raju	13-11-2019	
Training on Pipe bending machine	Sri Ch.Varun, MVGRCE(A)	Sri B.Linga Raju Sri G. Sanjeevi Naidu Sri. V. Raja Shekar	20-11-2019	
Training on Sand Testing machine	Sri Ch.Varun, MVGRCE (A)	Sri S.Ramana Murthy Sri B. Ganesh Sri. G. Koteswara Rao	20-11-2019	

### **Training on Lathe machine**

No of days: 1 day Dates: 13th Nov 2019

**Trainer:** Sri G.Pramod Asst. Prof, Dept of Mech Engg, MVGRCE

### **Participants:**

Sri K. Prasada Rao Sri V. Prasada Rao Sri. B. Linga Raju

