



# MVGR College of Engineering

(Autonomous)

Approved by AICTE, New Delhi and Permanently Affiliated to JNTU, Kakinada

Listed U/S 2(f) & 12(B) of the UGC Act 1956

Vijayaram Nagar Campus, Chintalavalasa, Vizianagaram - 535 005, Andhra Pradesh

30/01/2019

The following is the provisional list of successful candidates appeared for M.Tech. III Semester REGULAR EXAMINATIONS held in  
DECEMBER 2018

Branch: **STRUCTURAL ENGINEERING**

Passed in ALL SUBJECTS

17331D8701	17331D8702	17331D8703	17331D8704	17331D8705	17331D8706
17331D8707	17331D8708	17331D8709	17331D8710	17331D8711	17331D8712
17331D8713	17331D8714	17331D8715			

Passed in RESEARCH METHODOLOGY

-NIL-

Passed in COMPREHENSIVE VIVA-VOCE

-NIL-

Passed in PRE-REQUISITE STUDY

-NIL-

Passed in SEMINAR

-NIL-

Passed in PROJECT(PHASE - I)

-NIL-

**PRINCIPAL**

Principal

MVGR College of Engineering (A)  
Vizianagaram - 535 005



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**Branch: POWER SYSTEMS**

**Passed in ALL SUBJECTS**

17331D5601	17331D5602	17331D5603	17331D5604	17331D5605	17331D5606
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**Passed in RESEARCH METHODOLOGY**

-NIL-

**Passed in COMPREHENSIVE VIVA-VOCE**

-NIL-

**Passed in PRE-REQUISITE STUDY**

-NIL-

**Passed in SEMINAR**

-NIL-

**Passed in PROJECT(PHASE - I)**

-NIL-

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DECEMBER 2018

Branch: **PRODUCT DESIGN AND MANUFACTURING**

Passed in ALL SUBJECTS

17331D3401	17331D3402	17331D3404	17331D3405	17331D3406	17331D3407
17331D3408	17331D3409	17331D3410	17331D3411	17331D3412	17331D3413
17331D3414					

Passed in RESEARCH METHODOLOGY

--NIL--

Passed in COMPREHENSIVE VIVA-VOCE

--NIL--

Passed in PRE-REQUISITE STUDY

--NIL--

Passed in SEMINAR

--NIL--

Passed in PROJECT(PHASE - I)

--NIL--

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**DECEMBER 2018**

Branch: **COMMUNICATION SYSTEMS**

Passed in **ALL SUBJECTS**

17331D4701      17331D4702      17331D4703      17331D4704      17331D4705

Passed in **RESEARCH METHODOLOGY**

--NIL--

Passed in **COMPREHENSIVE VIVA-VOCE**

--NIL--

Passed in **PRE-REQUISITE STUDY**

--NIL--

Passed in **SEMINAR**

--NIL--

Passed in **PROJECT (PHASE - I)**

--NIL--

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30/01/2019

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DECEMBER 2018

Branch: VLSI

Passed in ALL SUBJECTS

17331D5701

17331D5702

17331D5703

17331D5704

17331D5705

Passed in RESEARCH METHODOLOGY

-NIL-

Passed in COMPREHENSIVE VIVA-VOCE

-NIL-

Passed in PRE-REQUISITE STUDY

-NIL-

Passed in SEMINAR

-NIL-

Passed in PROJECT(PHASE - I)

-NIL-

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30/01/2019

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Branch: COMPUTER NETWORKS AND INFORMATION SECURITY

Passed in ALL SUBJECTS

17331D8401      17331D8402      17331D8403

Passed in RESEARCH METHODOLOGY

-NIL-

Passed in COMPREHENSIVE VIVA-VOCE

-NIL-

Passed in PRE-REQUISITE STUDY

-NIL-

Passed in SEMINAR

-NIL-

Passed in PROJECT(PHASE - I)

-NIL-

**PRINCIPAL**

Principal

MVGR College of Engineering (A)

Vizianagaram - 535 005