



MVGR College of Engineering (Autonomous)

Approved by AICTE, New Delhi and Permanently Affiliated to JNTU, Kakinada
Listed U/S 2(f) & 12(B) of the UGC Act 1956
Vijayaram Nagar Campus, Chintalavalasa, Vizianagaram - 535 005, Andhra Pradesh

21/03/2019

The following is the provisional list of successful candidates appeared for **M.Tech. I Semester REGULAR EXAMINATIONS** held in
JANUARY 2019

Branch: COMPUTER NETWORKS AND INFORMATION SECURITY

Passed in ALL SUBJECTS

18331D8401 18331D8402 18331D8403 18331D8404

Passed in ADVANCED COMPUTER NETWORKS

--NIL--

Passed in ADVANCED DATA STRUCTURES

--NIL--

Passed in OOP USING JAVA

--NIL--

Passed in INFORMATION SECURITY

--NIL--

Passed in SENSOR AND ADHOC NETWORKS

--NIL--

Passed in NETWORK PERIMETER SECURITY

--NIL--

Passed in NETWORKING DESIGN LAB - I

--NIL--

PRINCIPAL
Principal

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Branch: POWER SYSTEMS

Passed in ALL SUBJECTS

18331D5602 18331D5605

Passed in POWER SYSTEM OPERATION AND CONTROL

18331D5603 18331D5607

Passed in HVDC TRANSMISSION

18331D5601 18331D5603 18331D5606 18331D5607

Passed in MODELING AND SIMULATION OF POWER ELECTRONIC SYSTEMS

--NIL--

Passed in RENEWABLE ENERGY SOURCES

18331D5601 18331D5603 18331D5604 18331D5606 18331D5607

Passed in MODERN CONTROL SYSTEMS

18331D5601 18331D5603 18331D5606 18331D5607

Passed in AI TECHNIQUES

18331D5601 18331D5603 18331D5606 18331D5607

Passed in POWER SYSTEMS LAB

18331D5601 18331D5603 18331D5604 18331D5606 18331D5607

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Branch: PRODUCT DESIGN AND MANUFACTURING

Passed in ALL SUBJECTS

18331D3401	18331D3402	18331D3403	18331D3404	18331D3405	18331D3406
18331D3407	18331D3408	18331D3409	18331D3410	18331D3411	18331D3412
18331D3413					

Passed in PRODUCT DESIGN

--NIL--

Passed in COMPUTER AIDED DESIGN

--NIL--

Passed in MATERIALS AND PROCESSES SELECTION

--NIL--

Passed in ADVANCED MANUFACTURING TECHNOLOGY

--NIL--

Passed in PROJECT MANAGEMENT

--NIL--

Passed in FLEXIBLE MANUFACTURING SYSTEMS

--NIL--

Passed in ADVANCED MANUFACTURING LAB

--NIL--

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JANUARY 2019

Branch: STRUCTURAL ENGINEERING

Passed in ALL SUBJECTS

18331D8701	18331D8702	18331D8703	18331D8704	18331D8706	18331D8707
18331D8708	18331D8709	18331D8710	18331D8711	18331D8712	18331D8714
18331D8718					

Passed in ADVANCED MATHEMATICS

18331D8705	18331D8713	18331D8716	18331D8717
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Passed in THEORY OF ELASTICITY

18331D8713	18331D8716
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Passed in ADVANCED REINFORCED CONCRETE

18331D8705	18331D8713	18331D8716	18331D8717
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Passed in STRUCTURAL DYNAMICS AND EARTHQUAKE RESISTANT DESIGN

18331D8705	18331D8713	18331D8716	18331D8717
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Passed in ADVANCED CONCRETE TECHNOLOGY

18331D8705	18331D8713	18331D8716	18331D8717
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Passed in DESIGN OF TALL STRUCTURES

18331D8705	18331D8717
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Passed in ADVANCED STRUCTURAL ENGINEERING LAB

18331D8705	18331D8713	18331D8716	18331D8717
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Branch: VLSI

Passed in ALL SUBJECTS

18331D5701	18331D5702	18331D5703	18331D5704	18331D5706	18331D5708
18331D5709	18331D5710	18331D5711	18331D5712		

Passed in VLSI TECHNOLOGY

18331D5705	18331D5707
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Passed in ANALOG IC DESIGN

18331D5705	18331D5707
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Passed in DIGITAL IC DESIGN

18331D5705	18331D5707
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Passed in CPLD AND FPGA ARCHITECTURE AND APPLICATIONS

18331D5705	18331D5707
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Passed in DIGITAL SYSTEM DESIGN

18331D5705	18331D5707
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Passed in DIGITAL DESIGN WITH VERILOG HDL

-NIL-

Passed in FPGA LAB

18331D5705	18331D5707
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